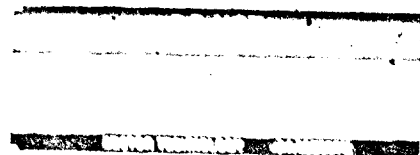


AD-A224 609



①

Final Report on the Project
for Development of New
Protocol Hardware and Software
for LSI-11 to Accommodate
AUTODIN II ADCCP-HDLC, and X.25

Appendix D

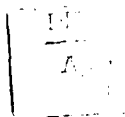
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Order No. 4024
Under Contract No.: MDA903-81-C-0038
Issued by Dept. of Army, Defense Supply Service
Washington, Washington, D.C. 20310

Submitted to: Defense Advanced Research Projects Agency
Information Processing Techniques Office
1400 Wilson Boulevard, Arlington, VA 22209

Submitted by: Associated Computer Consultants
720 Santa Barbara Street, Santa Barbara, CA 93101
(805)963-9431

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February 1983

MAINTENANCE MANUAL

A-1

A-1

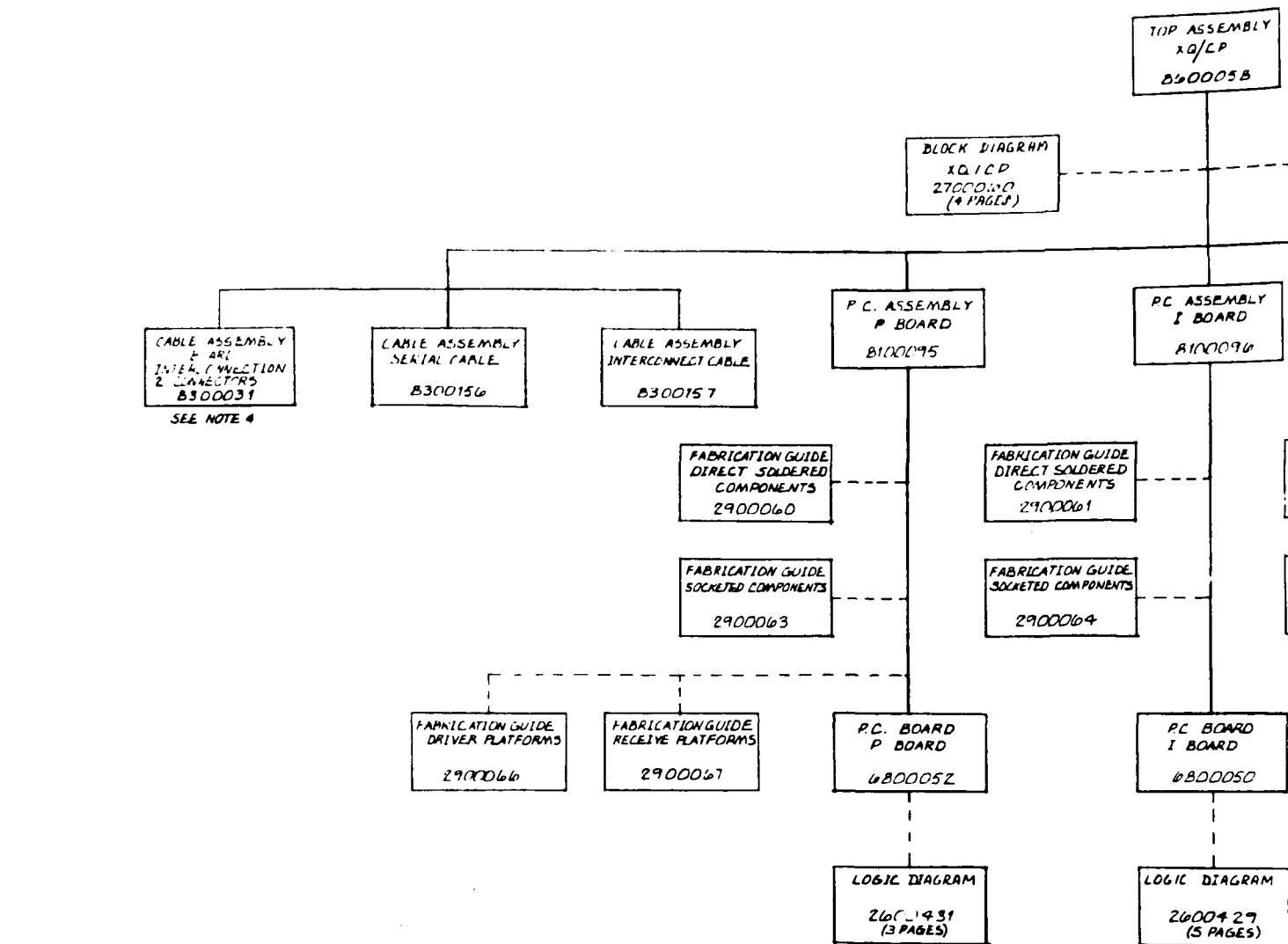


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B.0 DRAWING, DIAGRAMS AND SCHEMATICS

<u>Drawing Number</u>	<u>Revision Level</u>	<u>Date</u>	<u>Drawing Title</u>
3500020	C	9/82	Drawing Directory, XQ/CP
2700060 sheet 1	A	11/81	Block Diagram, XQ/CP
2700060 sheet 2	-	-	Block Diagram, XQ/CP, P Board
2700060 sheet 3	-	-	Block Diagram, XQ/CP, M Board
2700060 sheet 4	A	11/81	Block Diagram, XQ/CP, I Board
8100095 2 sheets	B	11/82	P.C. Assembly, XQ/CP, P Board
8100096 2 sheets	B-1	1/83	P.C. Assembly, XQ/CP, I Board
8100097 2 sheets	B-1	1/83	P.C. Assembly, XQ/CP, M Board
2600429 5 sheets	C	8/82	Logic Diagram, XQ/CP, I Board
2600430 2 sheets	C	8/82	Logic Diagram, XQ/CP, M Board
2600431 3 sheets	C-1	12/82	Logic Diagram, XQ/CP, P Board
2500027	A	9/82	Schematic, XQ/CP, RS-232, Dual Channel Only
2500028	A	9/82	Schematic, XQ/CP, RS-422, Dual Channel Only

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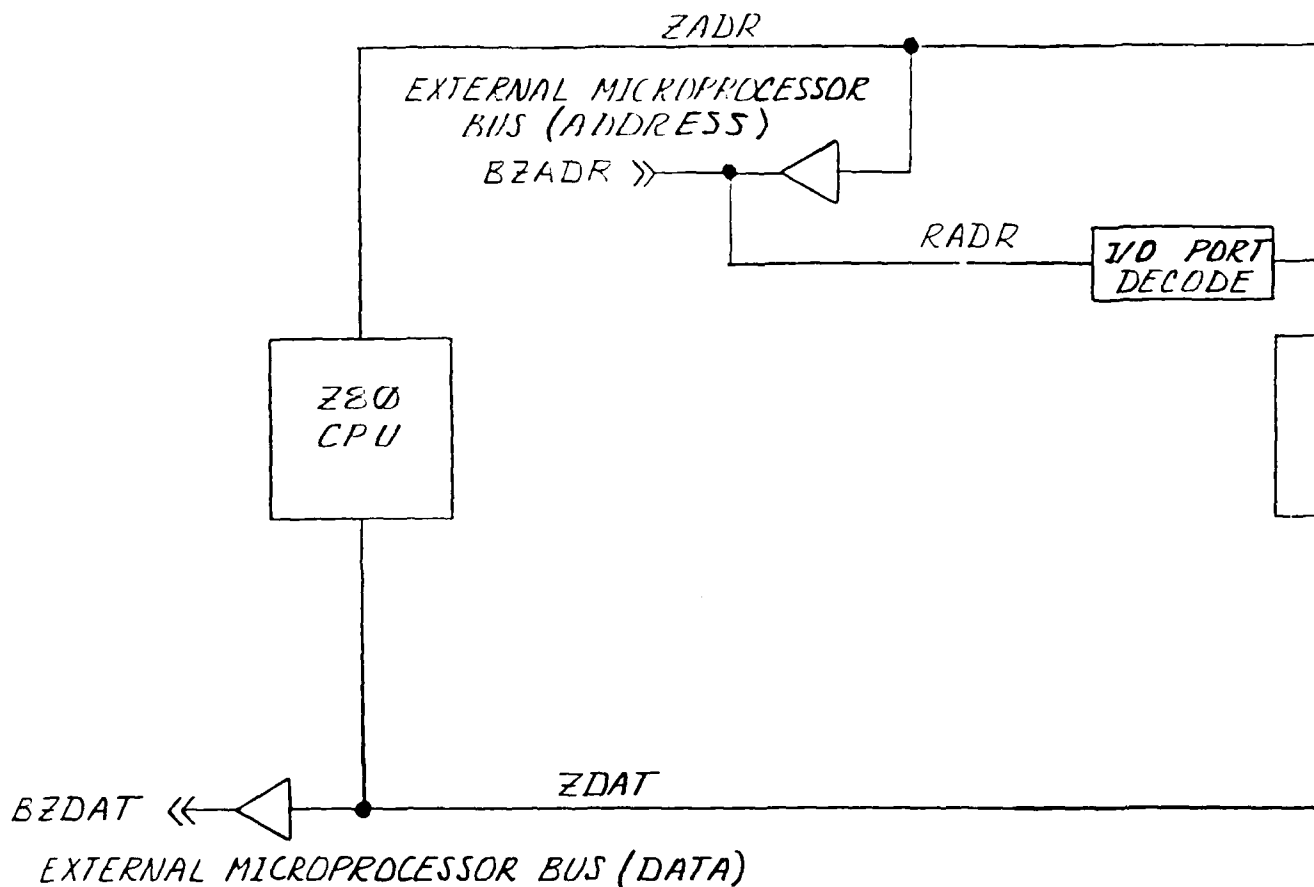


NOTES:

1. DOTTED LINES INDICATE REFERENCED DRAWINGS.
2. DOTTED BOXES INDICATE DRAWING DOES NOT EXIST.
3. DRAWING NUMBERS ARE SAME AS PART NUMBERS UNLESS OTHERWISE INDICATED.
4. USE APPROPRIATE MDMA BOARD INTERCONNECT CABLES:
B300032
B300033
B300034

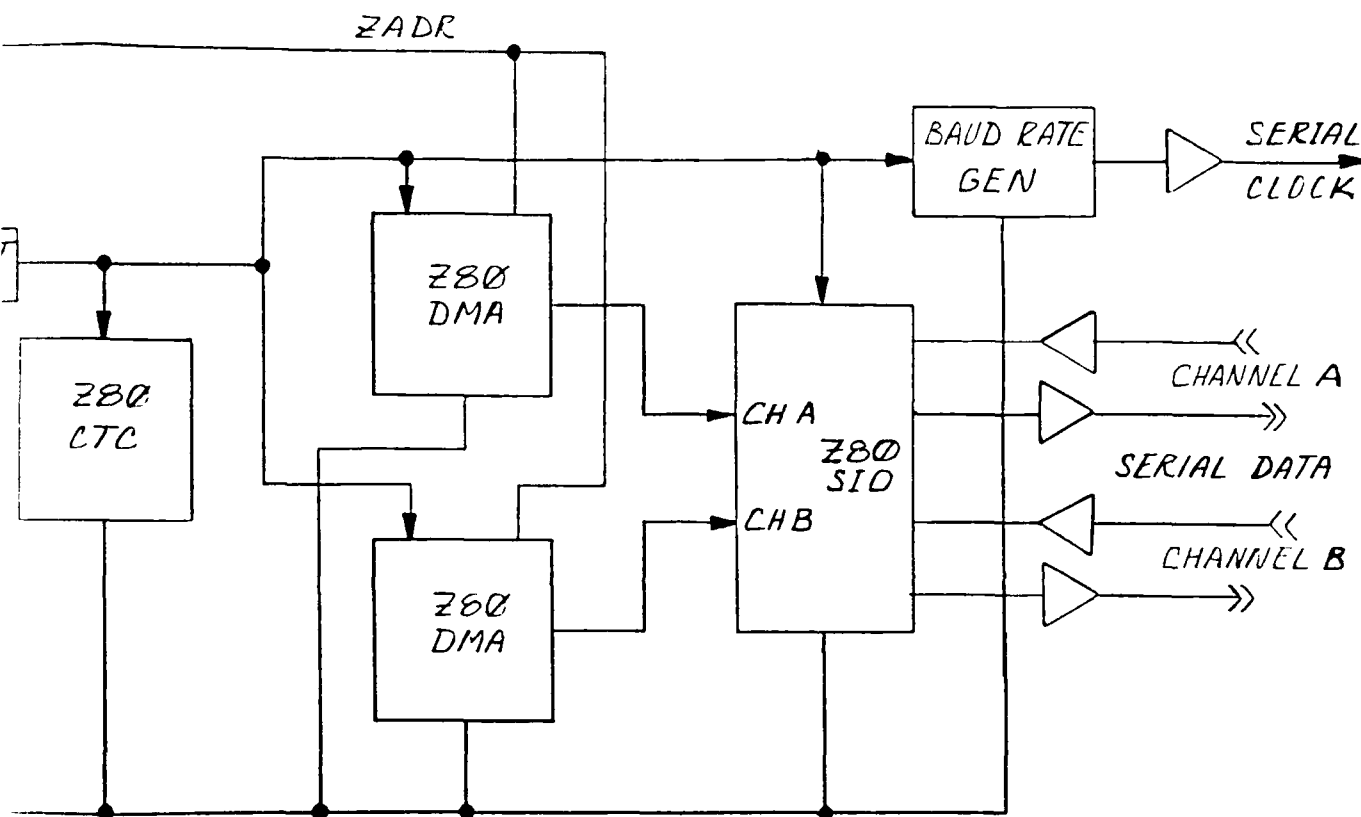
0200055

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REV	DESCRIPTION	DATE	APPROVED



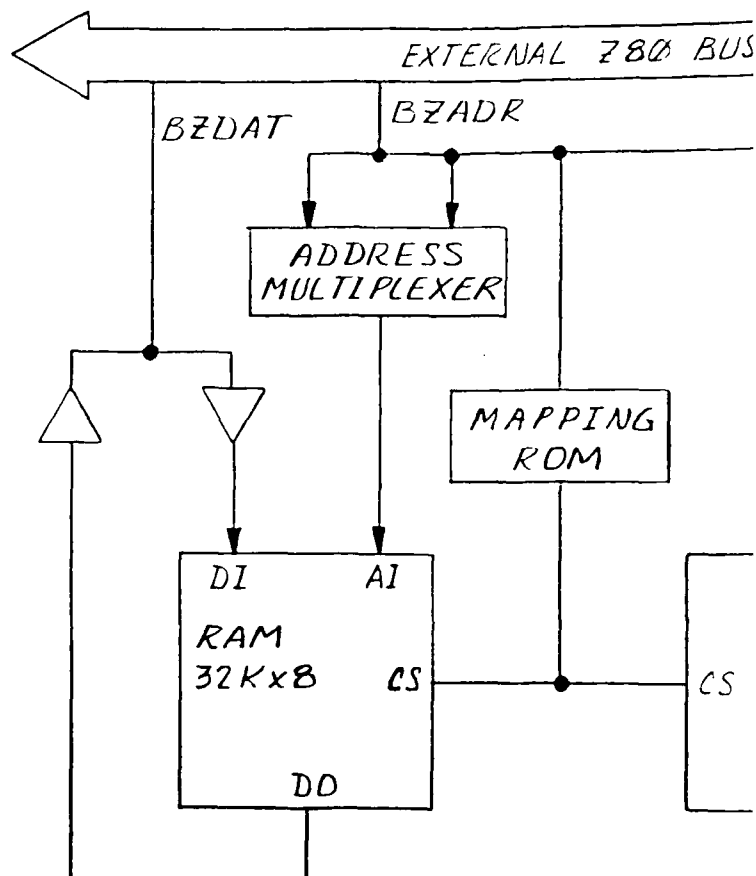
2700060

QTY REQD	FSCM NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MATERIAL SPECIFICATION
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PARTS LIST

OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: DIMENSIONS DECIMALS ANGLES .XX ± ± .XXX ± ± AL	CONTRACT NO.		ACC ASSOCIATED COMPUTER CONSULTANTS Santa Barbara, California 93101				
	APPROVALS		DATE		BLOCK DIAGRAM XQ/ICP PROCESSOR BOARD		
	DRAWN WEDDELL		1-10-81				
	CHECKED R. P. Russ		8 SEP 81				
ISSUED D. E. Lewis		10.19.81		SIZE B	FSCM NO.	DWG. NO. 2700060	REV.
DO NOT SCALE DRAWING		SCALE		SHEET 2 of 4			

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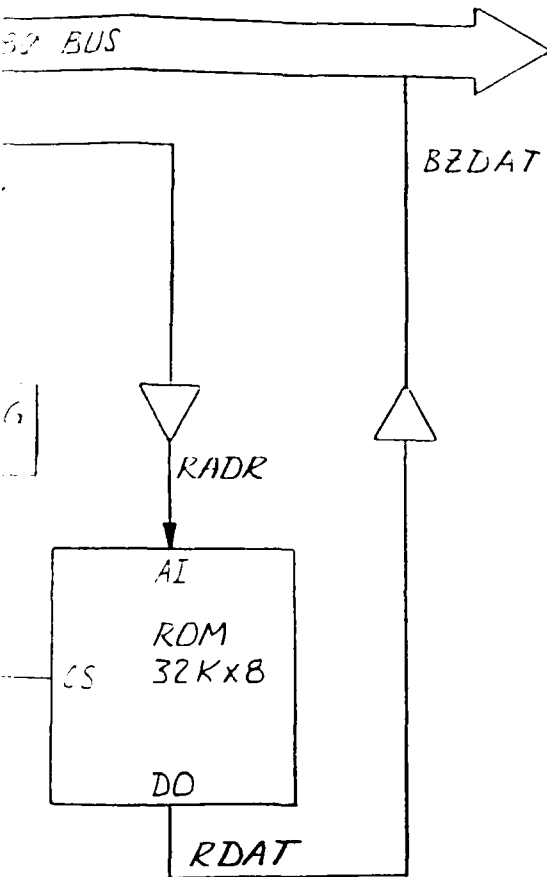
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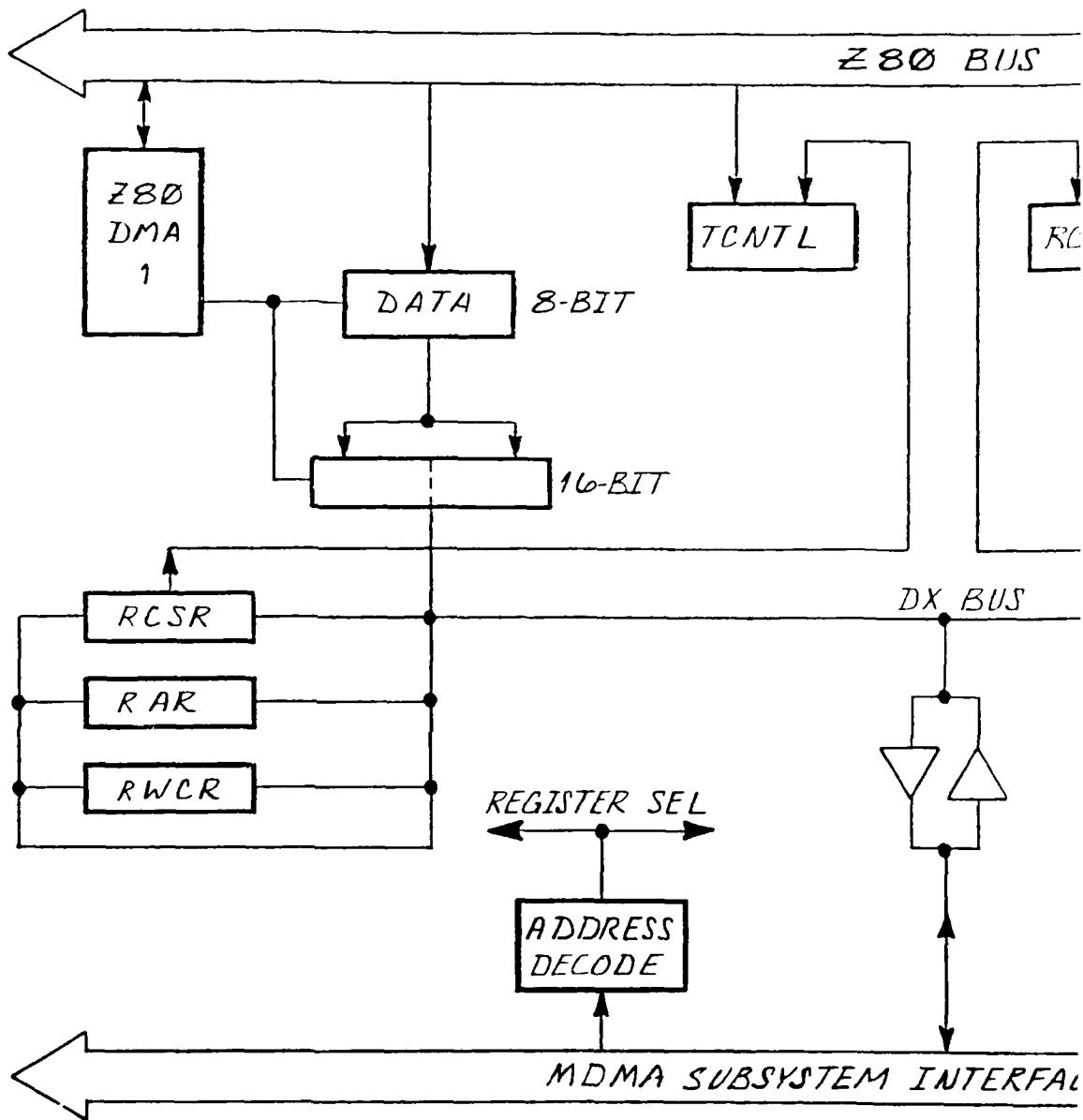
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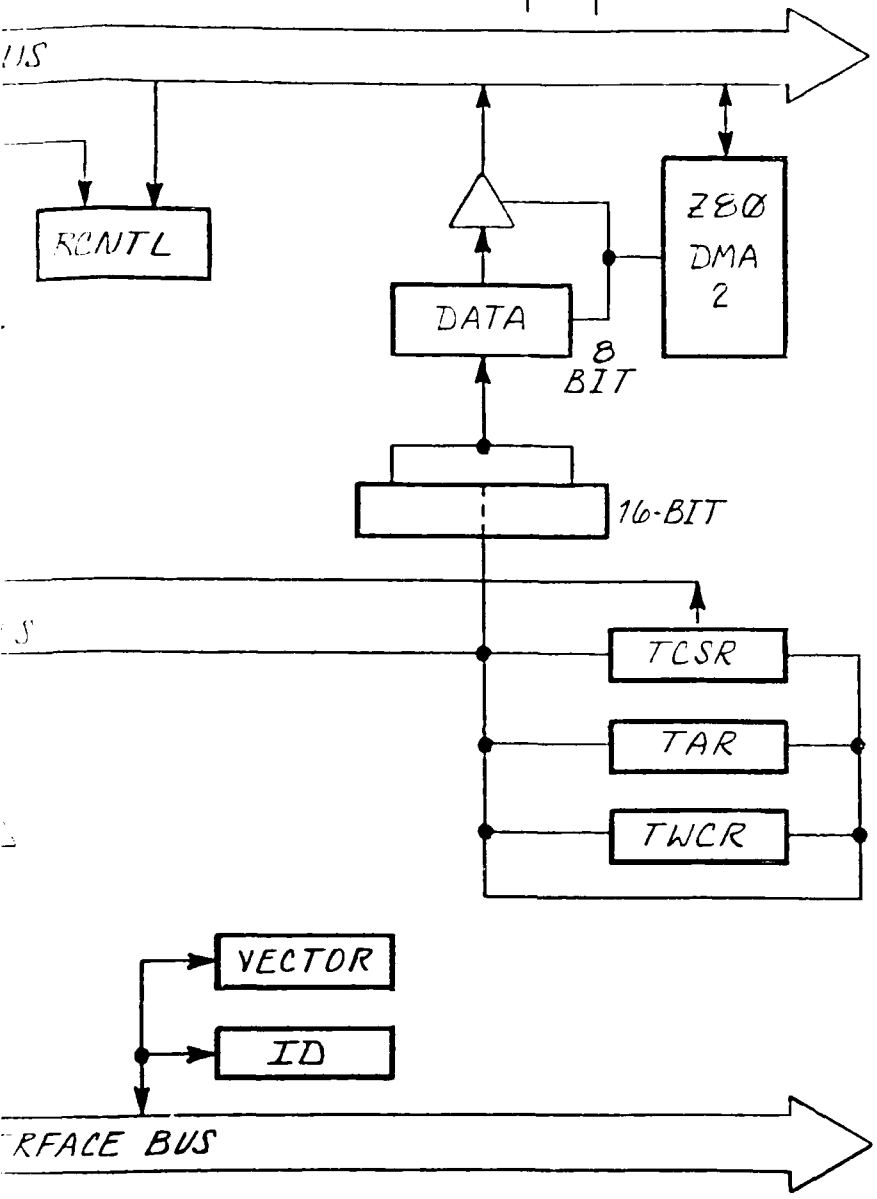
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	APPROVALS	DATE			
	DRAWN <i>Weddell</i>	9-1-81	BLOCK DIAGRAM XQ/CP MEMORY BOARD		
	CHECKED <i>R. Russ</i>	8 SEP 81			
ISSUED <i>J. E. Hunter</i>	10-19-81	SIZE B	FSCM NO.	DWG. NO. 2700060	REV.
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REV	DESCRIPTION	DATE	APPROVED
A	ADD "DMA" TO INTERFACE BUS	11.18.81	SR



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	CHECKED P. Russ		8 SEP 81	
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DO NOT SCALE DRAWING		SCALE		SHEET 4 of 4

A 2700060

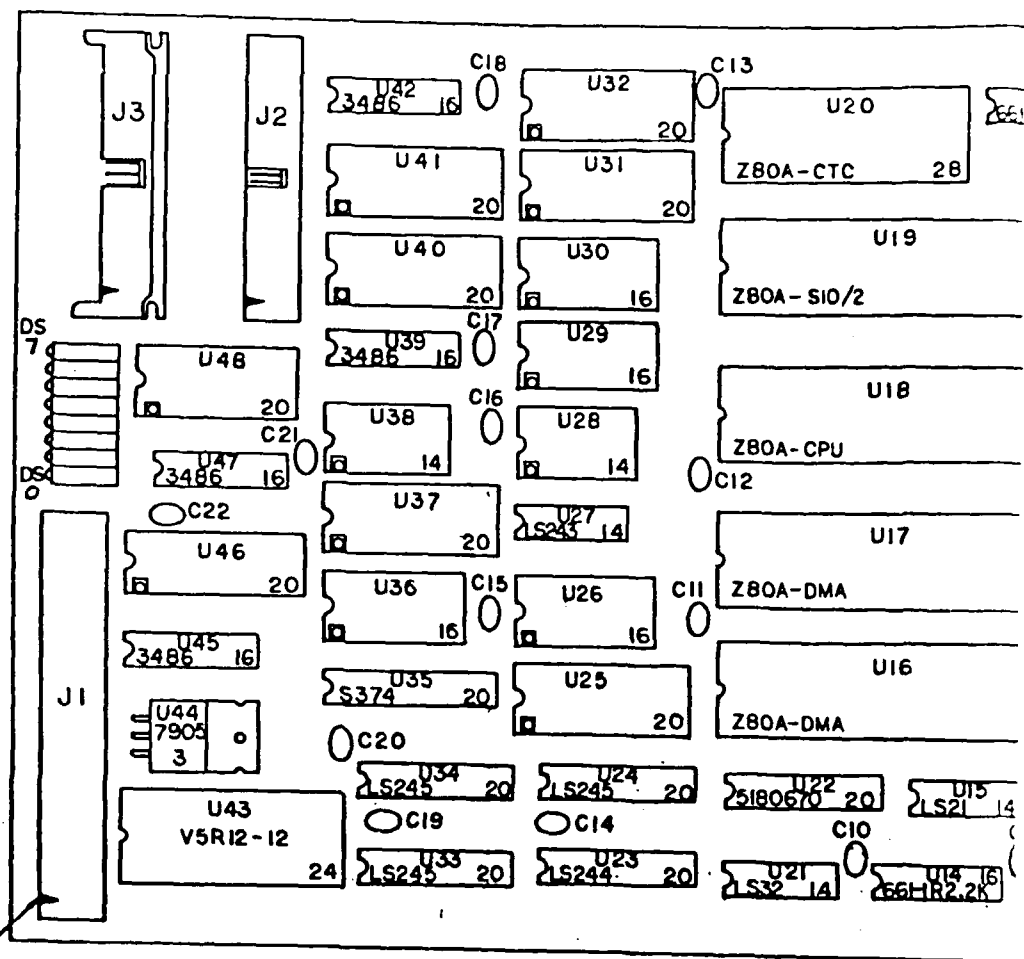
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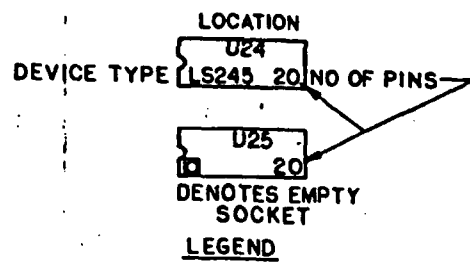
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PIN 1
(TYP)



COMPONENT SET	SEE INTE OP
5400012	RS
5400013	RS
5400014	RS
5400015	188 UNBAL
5400013	188 BAL

2. SEE 2600431 FOR LOGIC DIAGRAMS.

1. C3 THRU 22 ARE 0.01 μ F

NOTES: UNLESS OTHERWISE SPECIFIED

D



8100095 18

A

1. The first step in the process is to identify the problem. This involves a thorough understanding of the situation and the goals that need to be achieved. 2. Once the problem is identified, the next step is to develop a plan. This plan should outline the steps that need to be taken to solve the problem. 3. After the plan is developed, the next step is to implement it. This involves putting the plan into action and monitoring progress. 4. Finally, the last step is to evaluate the results. This involves assessing whether the goals have been achieved and whether the process was effective.

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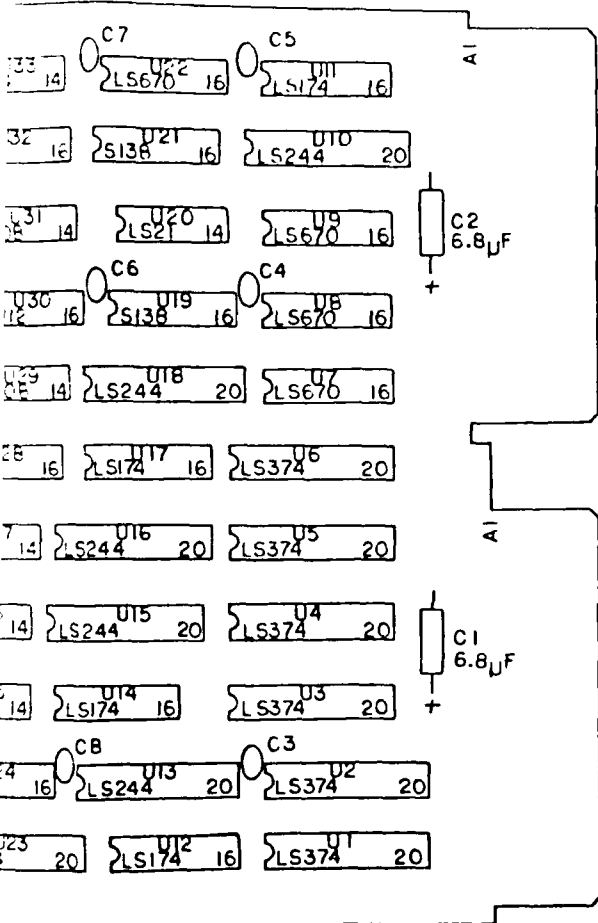
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8100095 28

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OF PINS

ITEM NO.	QTY	FROM	PART OR IDENTIFYING NO.	DESCRIPTION OR IDENTIFYING NO.	MATERIAL SPECIFICATION
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES FRACTIONS DECIMALS ANGLES .XXX .			CONTRACT NO.		
MATERIAL			APPROVALS		
FINISH			DATE		
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ISSUED			SCALE		
APPLICATION			DO NOT SCALE DRAWING		
8600058			PC ASSY XQ/CP I BD		
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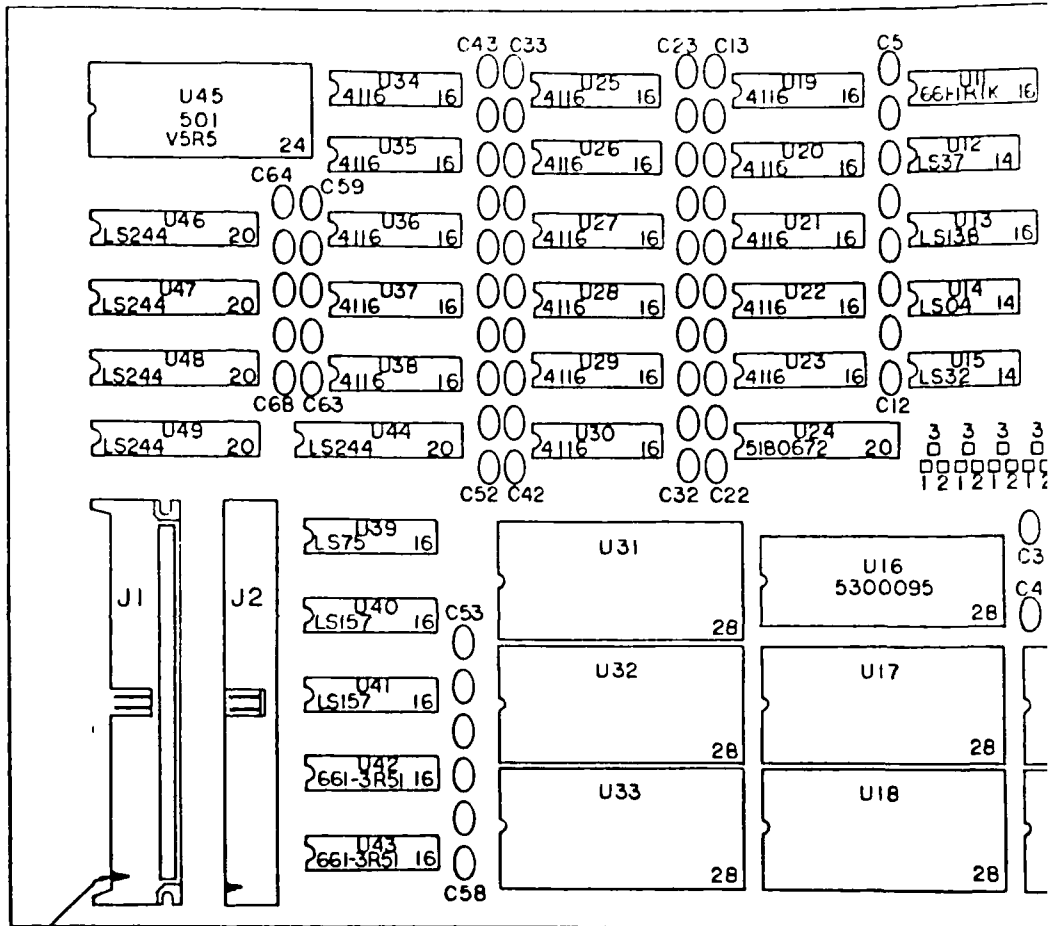
2600430

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B

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PIN 1
(TYP)

LOCATION
U23
DEVICE TYPE 4116 16
LEGEND

2. SEE 2600430 FOR LOGIC DIAGRAMS
1. C3 THRU 68 ARE 0.01μF

NOTES: UNLESS OTHERWISE SPECIFIED

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↑

27	2	0700104	CAP, TANT AX G.B 4# 35V 10%	C1,2
26	66	0700098	CAP, CER RAD. 0.1uF 100V GEN PUR	C3-6B
25	1	5300095	EPROM, XQ/CP IOX MAINT LOC UI6	U16
24	1	5100672	PROM MEMORY MAP	U24
23	2	0400052	DIP, B RES 51 OHM 2% 16P	U42,43
22	1	0400049	DIP, 15 RES 1KOHM 2% 16P	U11
21	1	0300165	CONN, 90 DEG 40P ANSLEY	J2
20	1	0200077	CONN, DC 15V IN +/- 5V OUT	U45
19	16	0100224	TMS4116-20 16K DYNAMIC RAM	U19,23,25-30,34-38
18	5	0100150	74LS244 OCT BUF DRV NONINV	U44,46-49
17	2	0100135	74LS157 QUAD 2-IN MUX	U40,41
16	1	0100131	74LS138 3 TO 8 LINE DEC/DEMUX	U13
15	1	0100118	74LS75 4-BIT BILTABLE LATCHES	U39
14	1	0100117	74LS74 DUAL D EDGE-TRIG F-F	U5
13	2	0100064	74LS74 DUAL D EDGE-TRIG F-F	U2,7
12	1	0100113	74LS37 QUAD 2-IN NAND BUF	U12
11	2	0100112	74LS32 QUAD 2-IN OR GATE	U3,15
10	1	0100009	7406 HEX INV BUF/DRV	U8
9	3	0100102	74LS04 HEX INV	U4,6,14
8	1	0100100	74LS02 QUAD 2-IN NOR GATE	U1
7	B	0300078	DIP SOCKET, 28P	
6	6	0300075	DIP SOCKET, 20P	
5	23	0300073	DIP SOCKET, 16P	
4	11	0300072	DIP SOCKET, 14P	
3	12	0600011	WIRE-WRAP STAKES	
2	1	0300182	CONN, 90 DEG 40 PIN 3M/BERG	J1
1	1	6800051	PC BD, XQ/CP MEMORY	

ITEM NO		QTY	FROM NO	PART OR IDENTIFY YOUR NO	DESCRIPTION	REF DISPOSITION
PARTS LIST						
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8100097

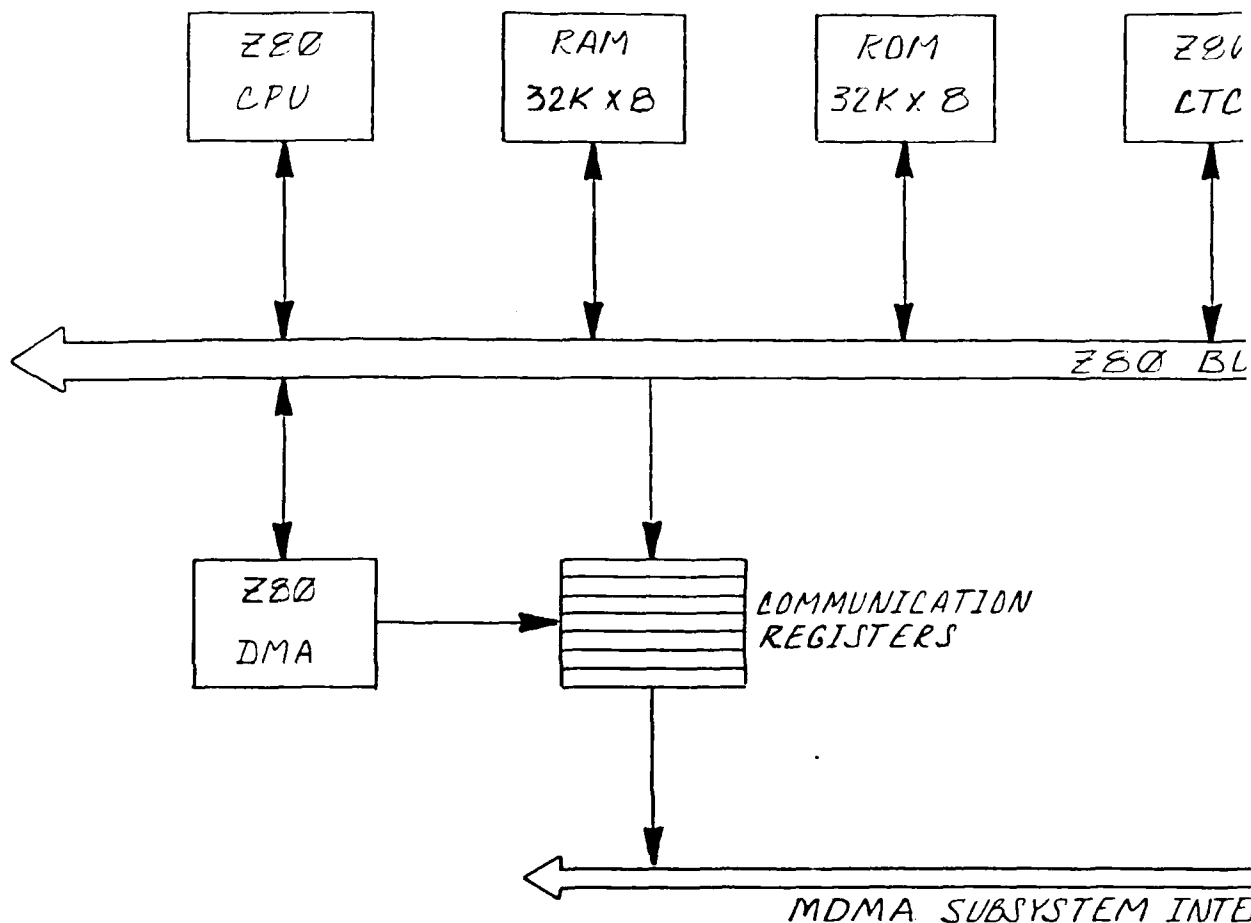
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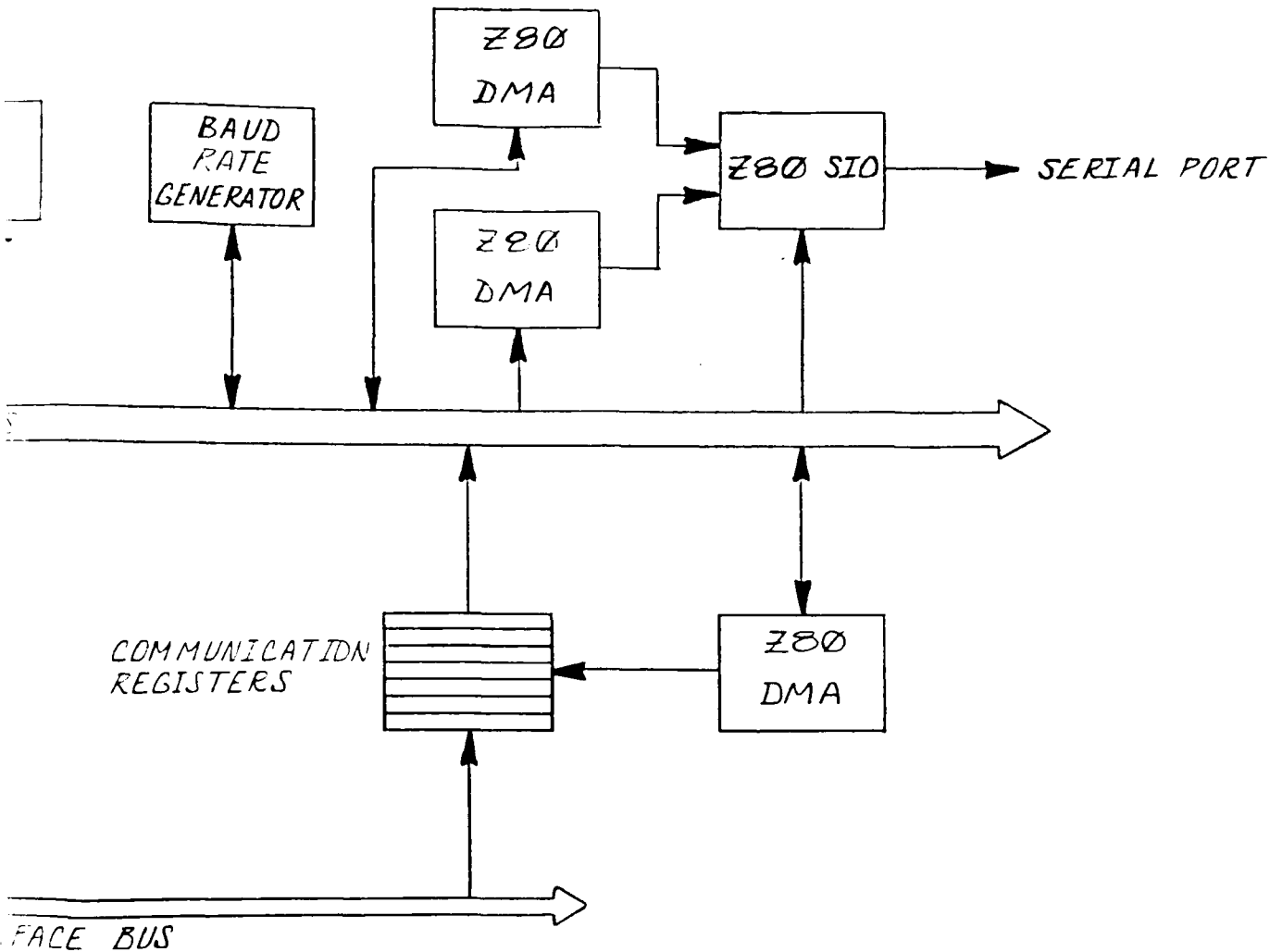
DWG. NO. 2700060

SM 1

REV A

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	ADD "MDMA" TO INTERFACE BUS. <i>SR</i>	11.18.81	<i>CW</i>



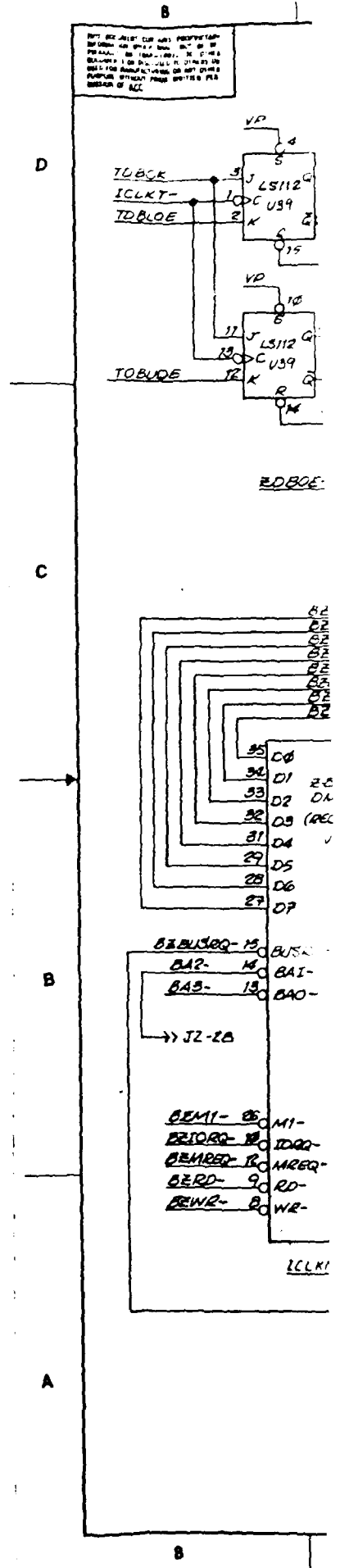
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2700060

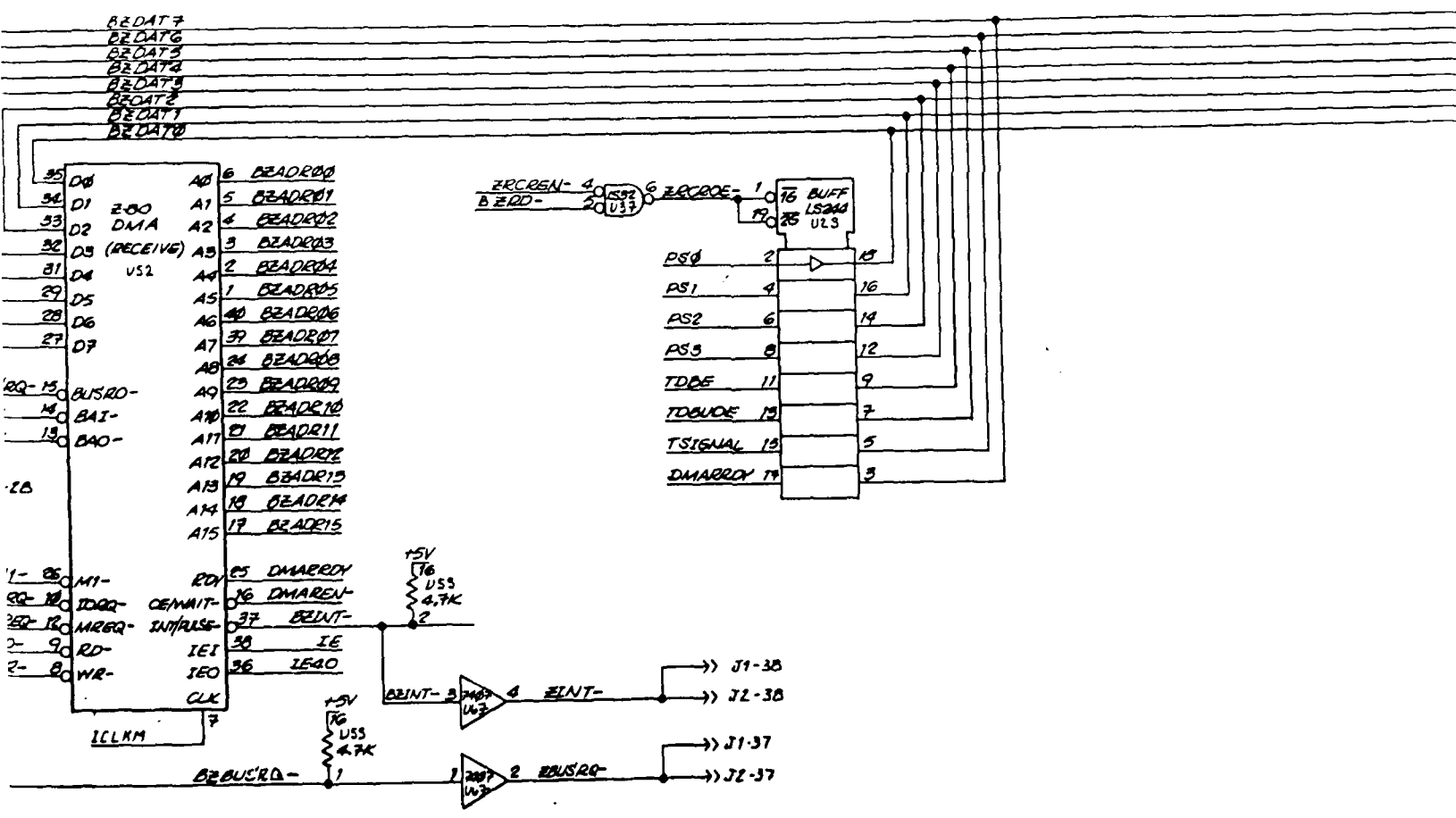
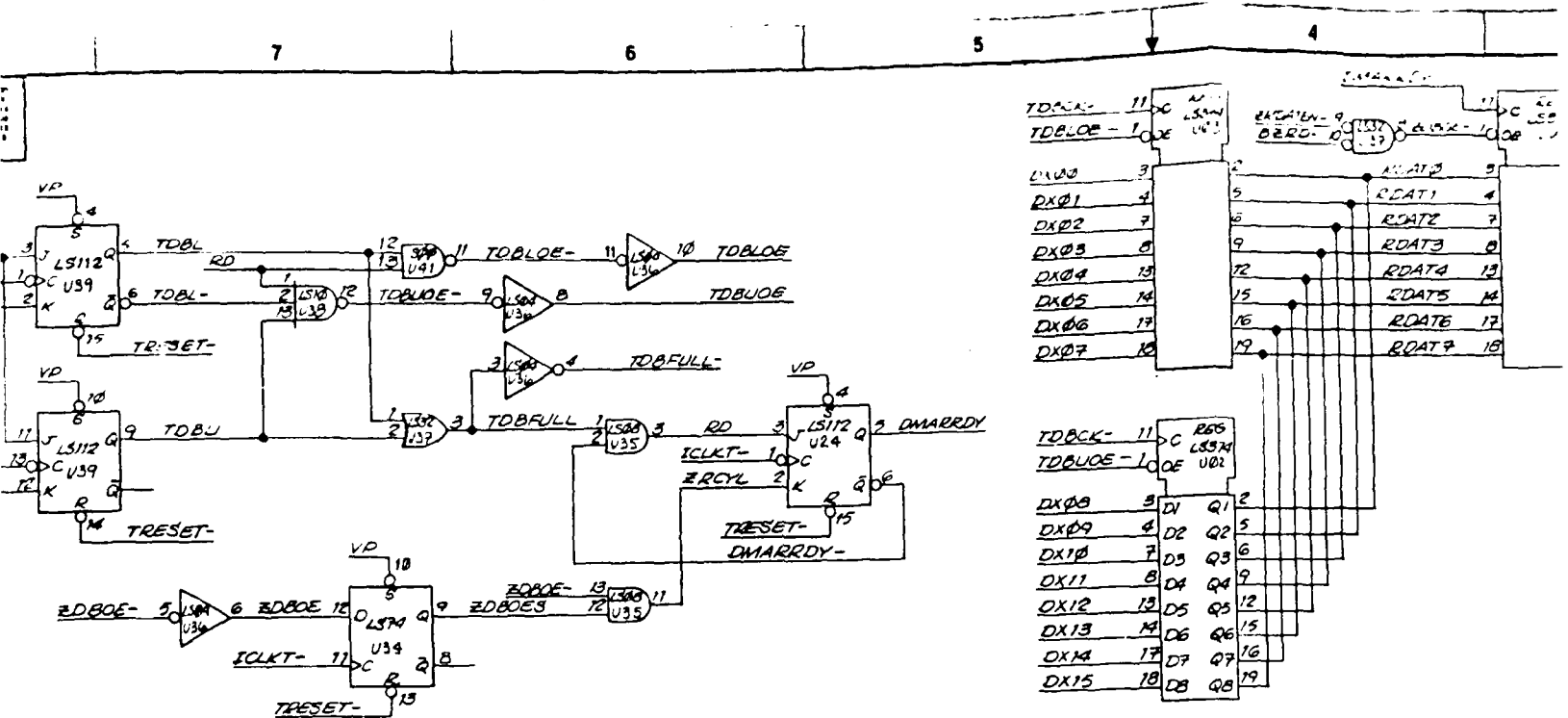
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INCHES DECIMALS XX ± XXX ±	APPROVALS DRAWN <i>WEDDELL</i> CHECKED <i>R. R. R.</i> ISSUED <i>D. E. Lawton</i>		DATE 2.9.81 8 SEP 81 10.11.81	
NOT SCALE DRAWING			SIZE B	FSCM NO. DWG. NO. 2700060 REV. A
			SCALE	SHEET 1 of 4

LOCAL NAME IDENTIFICATION - CONTINUED LOCALITY
PAGE 11 OF 15

* * * * * CROSSED AT INDIVIDUAL IDENTIFICATION - PAGE

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SV	A7 B7	03 04	RDAT5	L4*	
BA2-	B6*		RDAT6	D4*	
BA3-	B6		RDAT7	E4*	
BCADR00	C7*	03*	TDBCK	E8	
BCADR01	B7*	03*	TDBCK-	D5	04*
BCADR010	B7*	03*	TCBE	B6	04*
BCADR02	B7*		TDBFULL	E6*	05
BCADR03	B7*		TDBFULL-	D6*	
BCADR04	B7*	03*	TDBL	D7*	05
BCADR05	B7*	03*	TDBL-	D7*	
BCADR06	B7*	03*	TDBLOE	D6*	D8
BCADR07	B7*	03*	TDBLOE-	D5	D6*
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BCADR15	B7*	03*	TSIGNAL	B6	
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BZDAT0	C3 C7*			D8	
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BZINT-	A7 B7*		ZRCROE-	B6*	03* 05
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BZRC-	A6* B6	03*			
	D4				
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DMARRDY	B6 B7	03*			
	D4 D5*				
DMARRDY-	C6*				
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		05			
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IE	A7				
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RDAT1	D4*				
RDAT2	D4*				
RDAT3	D4*				
RDAT4	D4*				





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AB3	C3	04*	
BEMG-	A7*	04*	
BIAN-	A7*		
DX00	B6* C2*		
	C3		
DX01	B6* C2*		
	C3		
DX02	B6* C2*		
	C3		
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	C3		
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	D6*		
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	D6*		
DX15	A2* A3		
	D6*		
FNB2	D6		
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RCAE-	C4	04*	
RDREG-	C3*	04*	
RWCK-	C4		
RWCE-	C4	04*	
SEL-	D6	04*	
TCACK-	C4	04	
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XBAD03	C7*	04	
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XBRDY-	B7*		
XBRST	C7*	04*	

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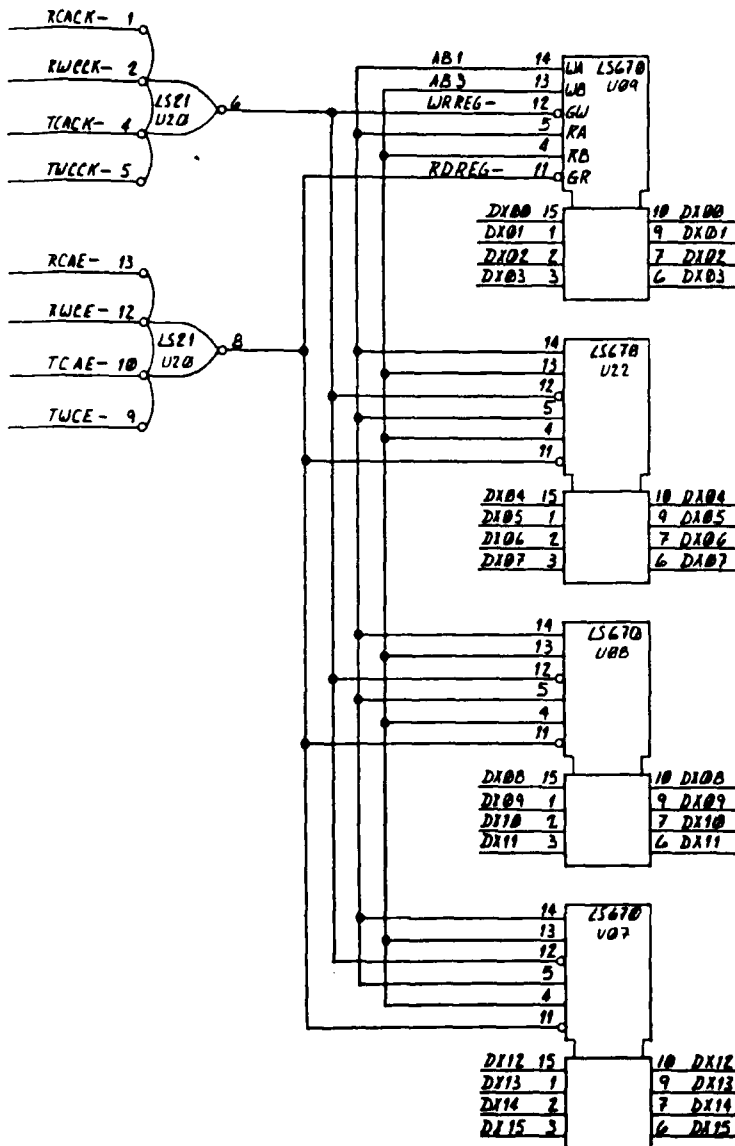
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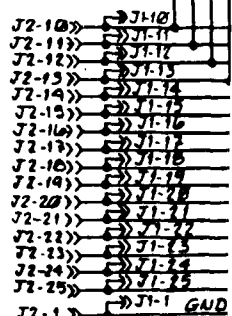
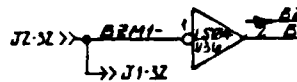
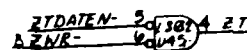
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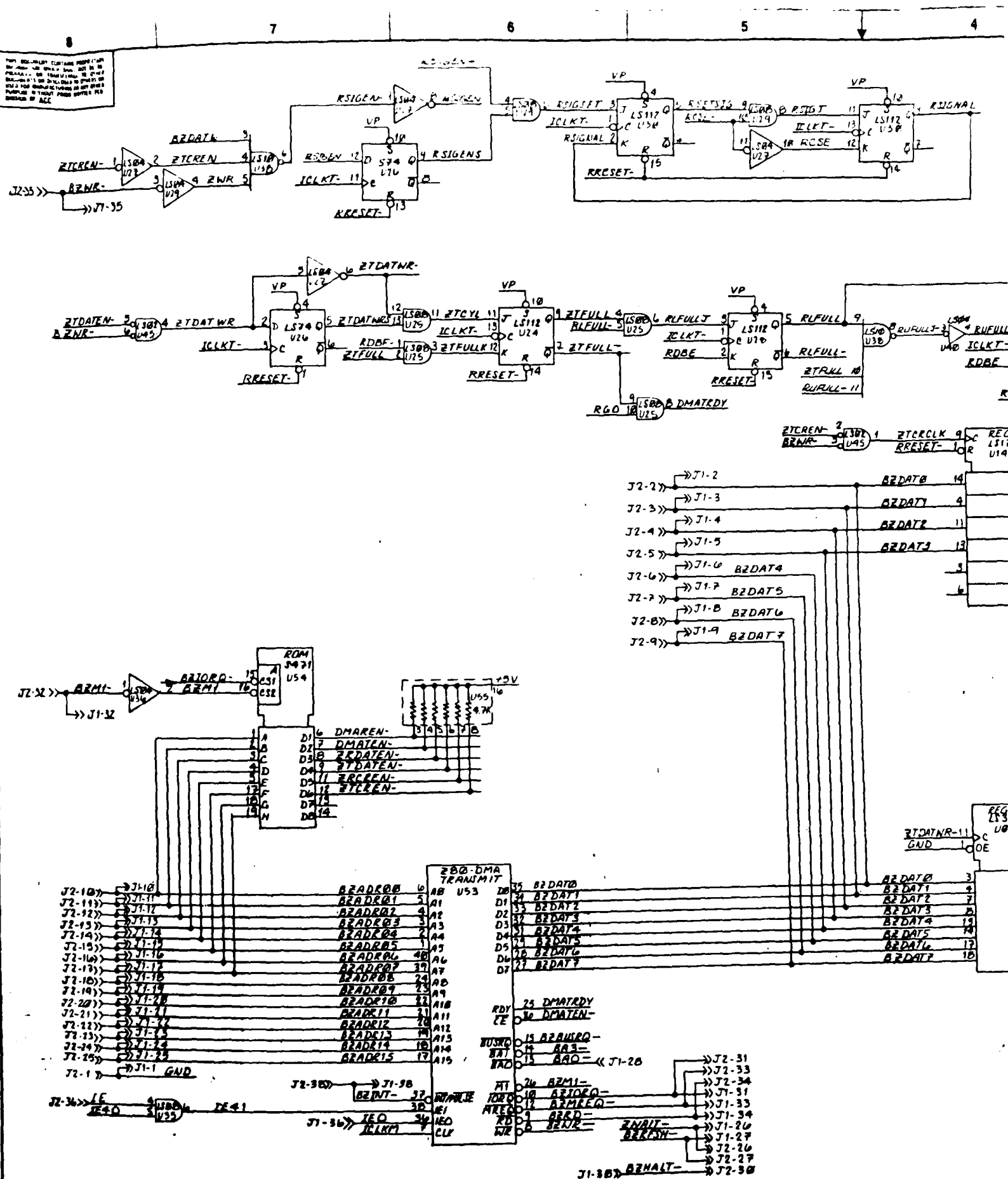
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	C	ECD B2229-05	12/30/62	



CONTRACT NO.		APPROVALS		DATE		ASSOCIATED COMPUTER CONSULTANTS Sunnyvale, California 95081	
DRAWN RICHMETTO		5-29-61		CHECKED R. R. R.		8-3-61	
DESIGNED J. S. R.		JUN 8 1961		FILE NO. D 7N084		DPO NO. 2600429	
APPLICATION		NEXT ADV.		USED ON		SHEET 2 OF 5	

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NAME: PART: DATE:

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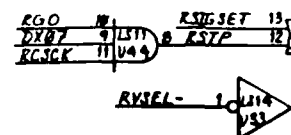
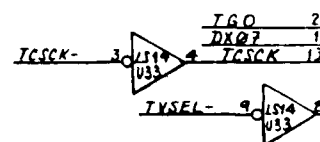
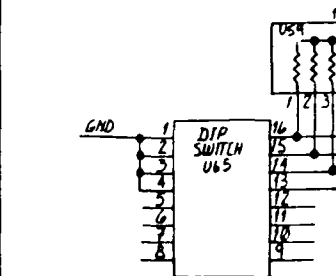
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AB3	C6	05	VECTA-	B3	D4	
AJ4	C7		VP	A7	B5	
AJ5	C7			B7	C5	
AJ6	C7			C7	D5	
AJ7	C7		WRTFN-	A5	B5	
BCKB-	B5	C4	XBAD01	B7		
	C6		XBAD02	B7	02	
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DX01	B2	D4 01 02	XBAD05	C7	02	
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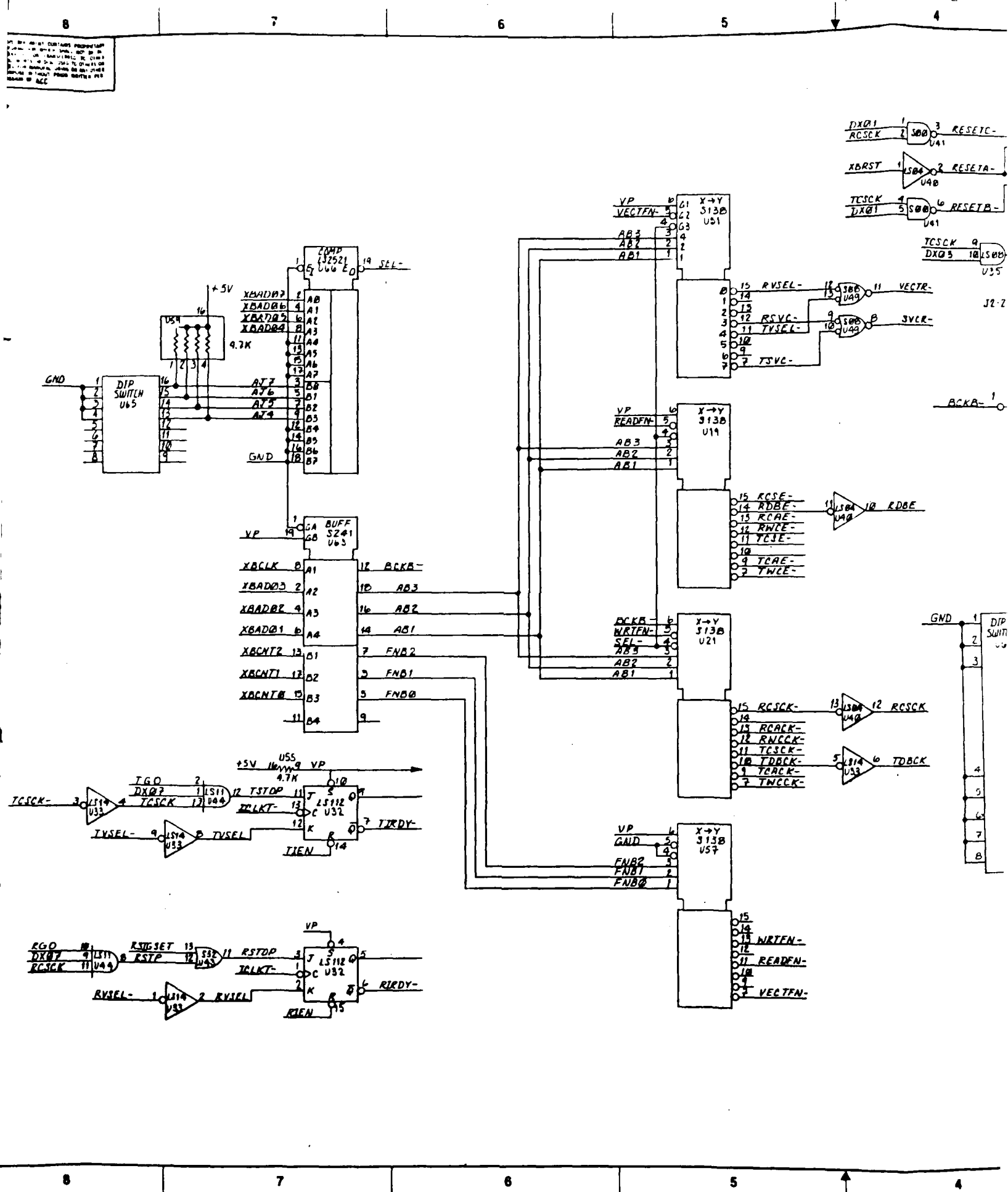
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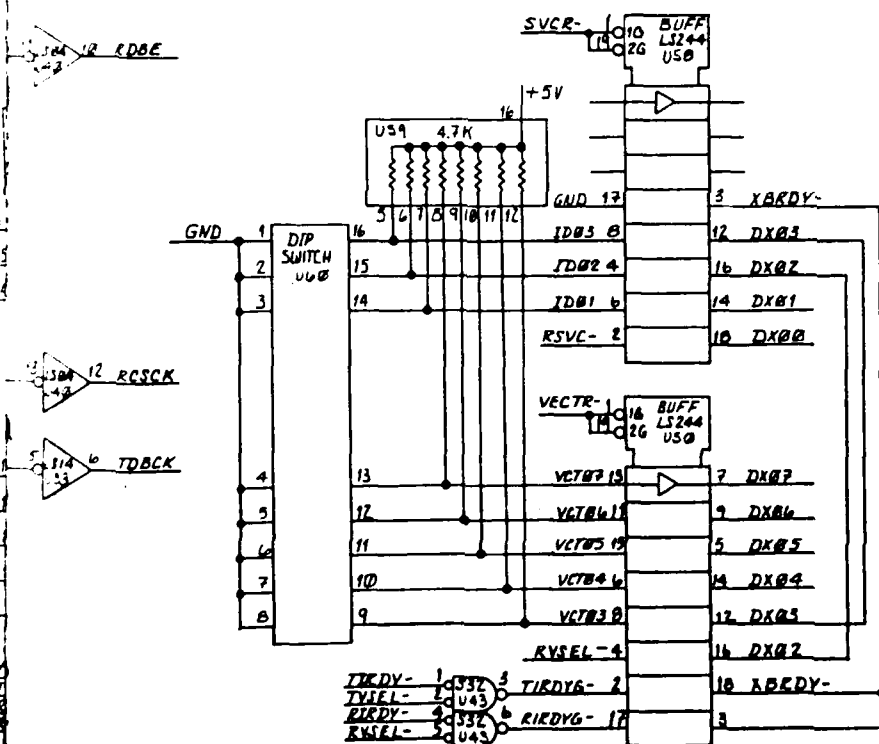
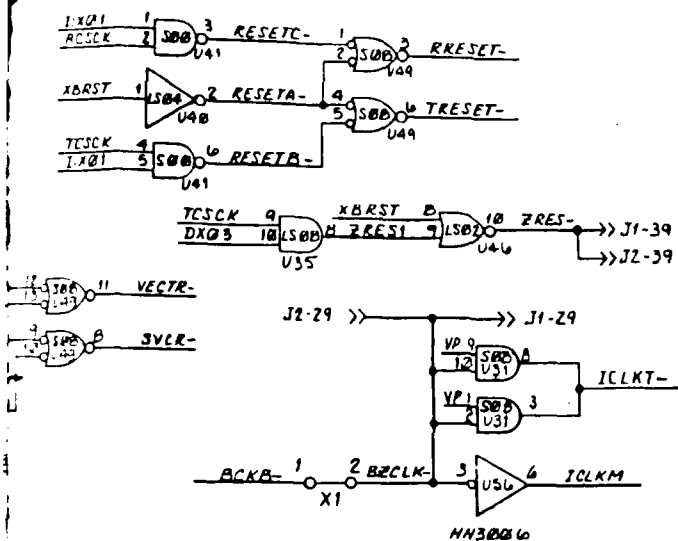
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FORM NO. 2600429		REV. 4	REV. 1	
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2:11	A	INITIAL LOCATION AT PERS	10/20/81	80
B-2	B	CHANG. VOLT TO +5V AT USS	10/20/81	80
	C	ECO 02229-05	10/20/82	



8100096	USED ON
NEXT ARMY	USED ON
APPLICATION	

CONTRACT NO.	
APPROVALS	DATE
DESIGNER PJ MEDDELL	12/19/80
CHECKER RQ	8/2/81
APP. NO.	8/2/81

ASSOCIATED COMPUTER CONSULTANTS Santa Barbara, California 93101	
LOGIC DIAGRAM	
XQ/CP I BOARD	
VECTOR, ID, SELECT, CLOCK	
DESIGN NO. 7N084	REV. NO. 2600429
SCALE / NAME	SHEET 4 OF 5

100% NAME LOCATION - 100% LOCATED

• - FULL OF INFORMATION - FIFTEEN PAGES

THIS SUBJECT'S CAR WAS STOPPED AND
SEARCHED ON 10-10-68 BY SA
PERALTA OF SAN JUAN. NO OTHER
REMARKS OF INTEREST TO CASE
WAS MADE. SUBJECT IS NOT TO BE
USED FOR TRAINING PURPOSES OR FOR
PURPOSES WITHOUT PRIOR WRITTEN
INSTRUCTION OF ACP.

DX 42

1.81416

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DA 49

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DXB

DXAP

NAME	CODE	ALT PAGE	NAME	CODE	ALT PAGE
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BZRD-	C3		ZS2	B4	03
CLRTSIG . . .	C1	C2	ZS3	B4	
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	D8				
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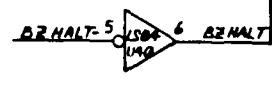
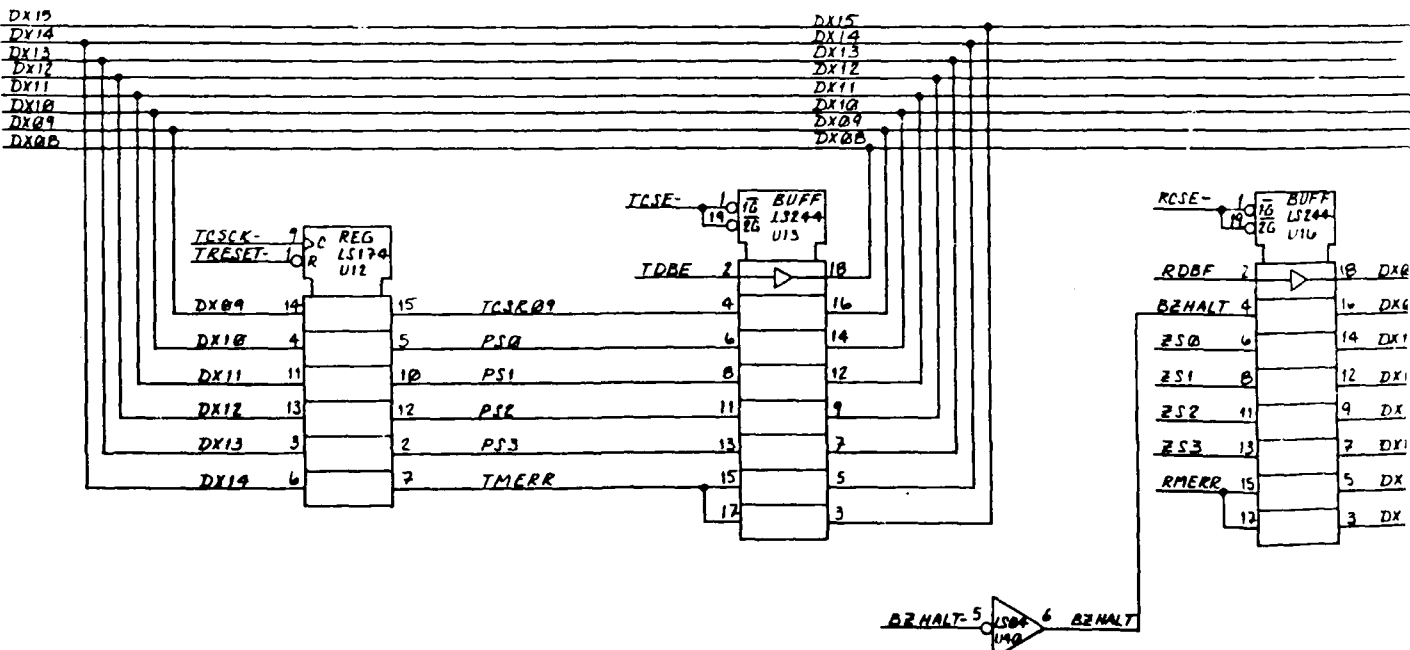
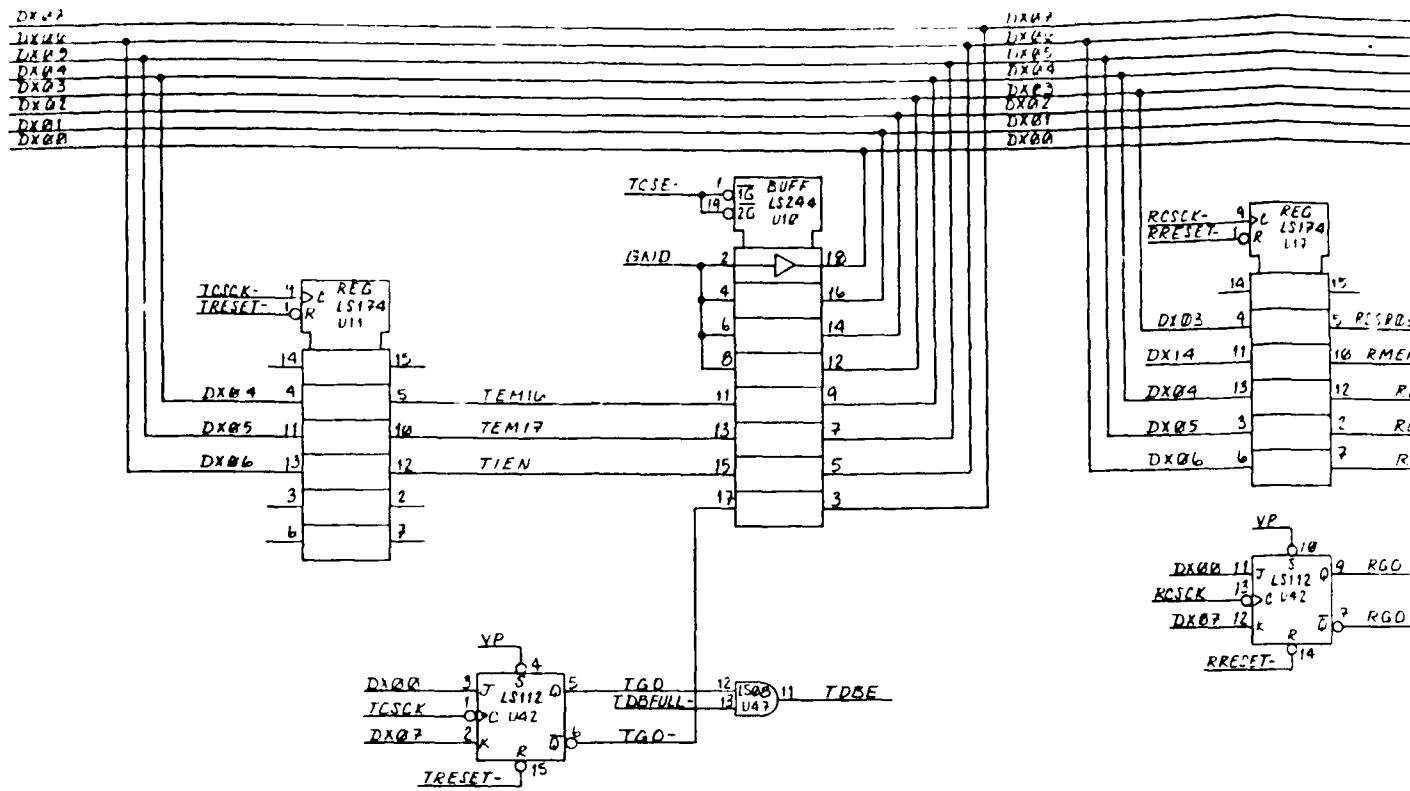
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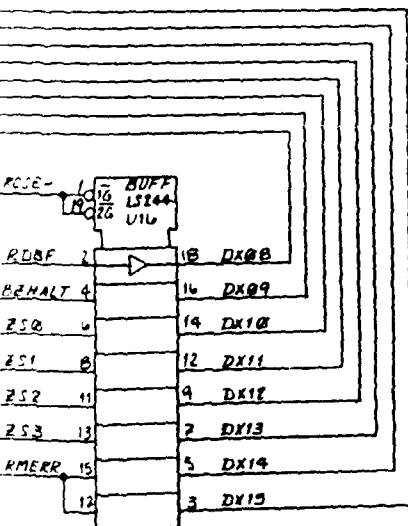
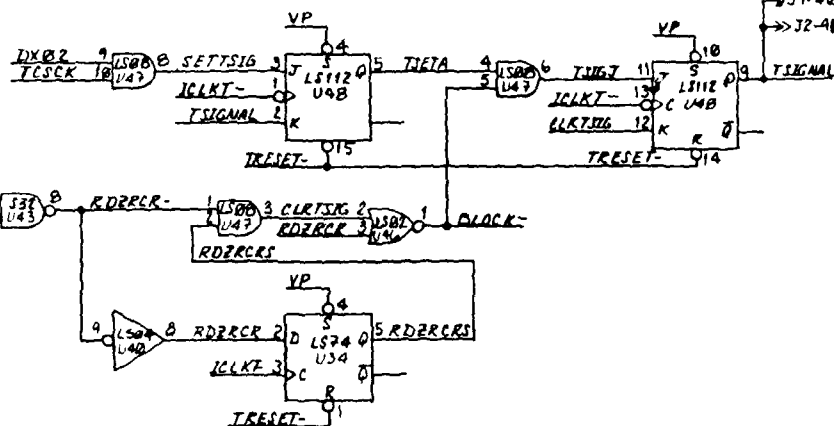
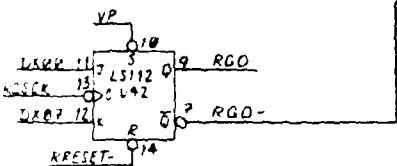
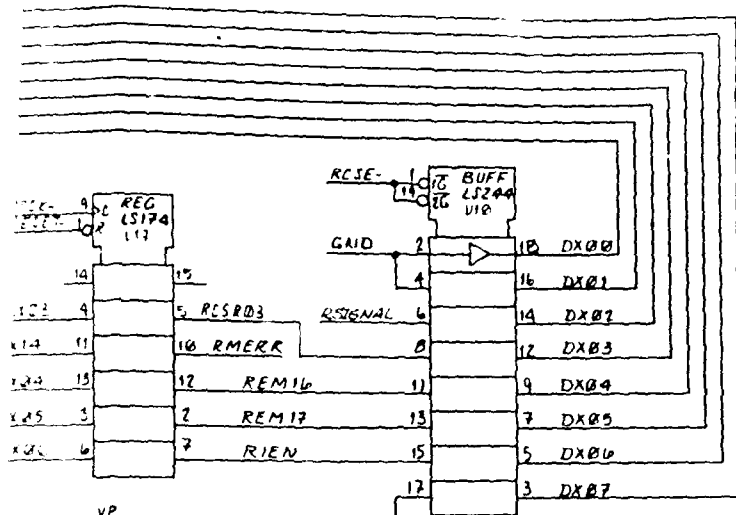
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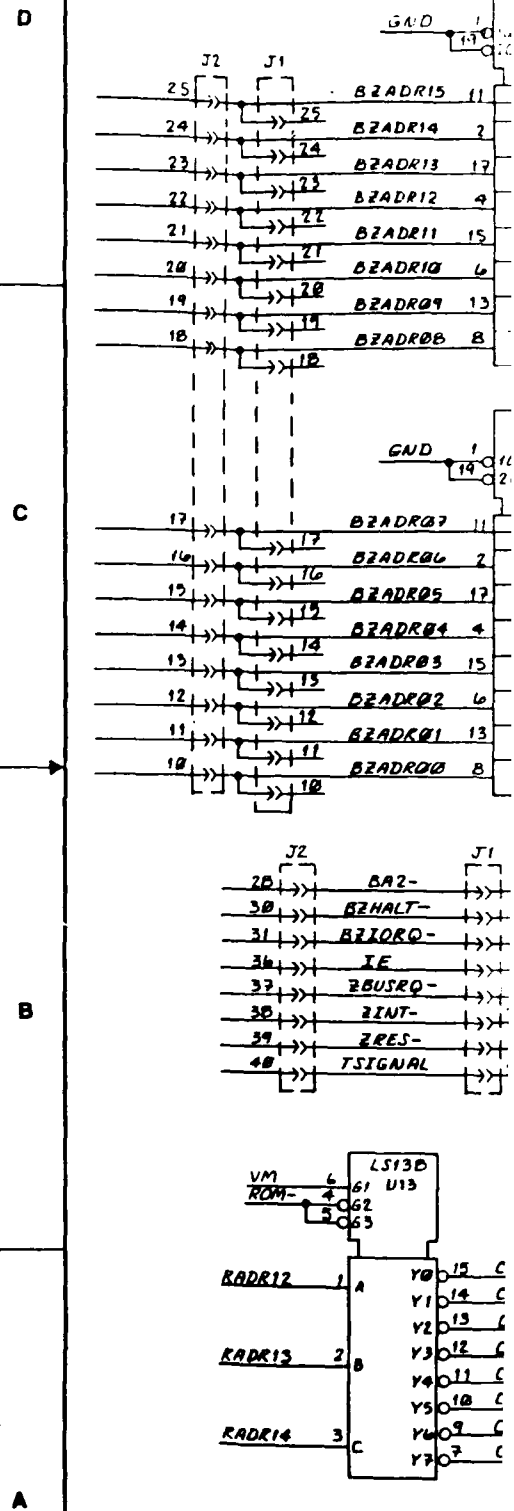


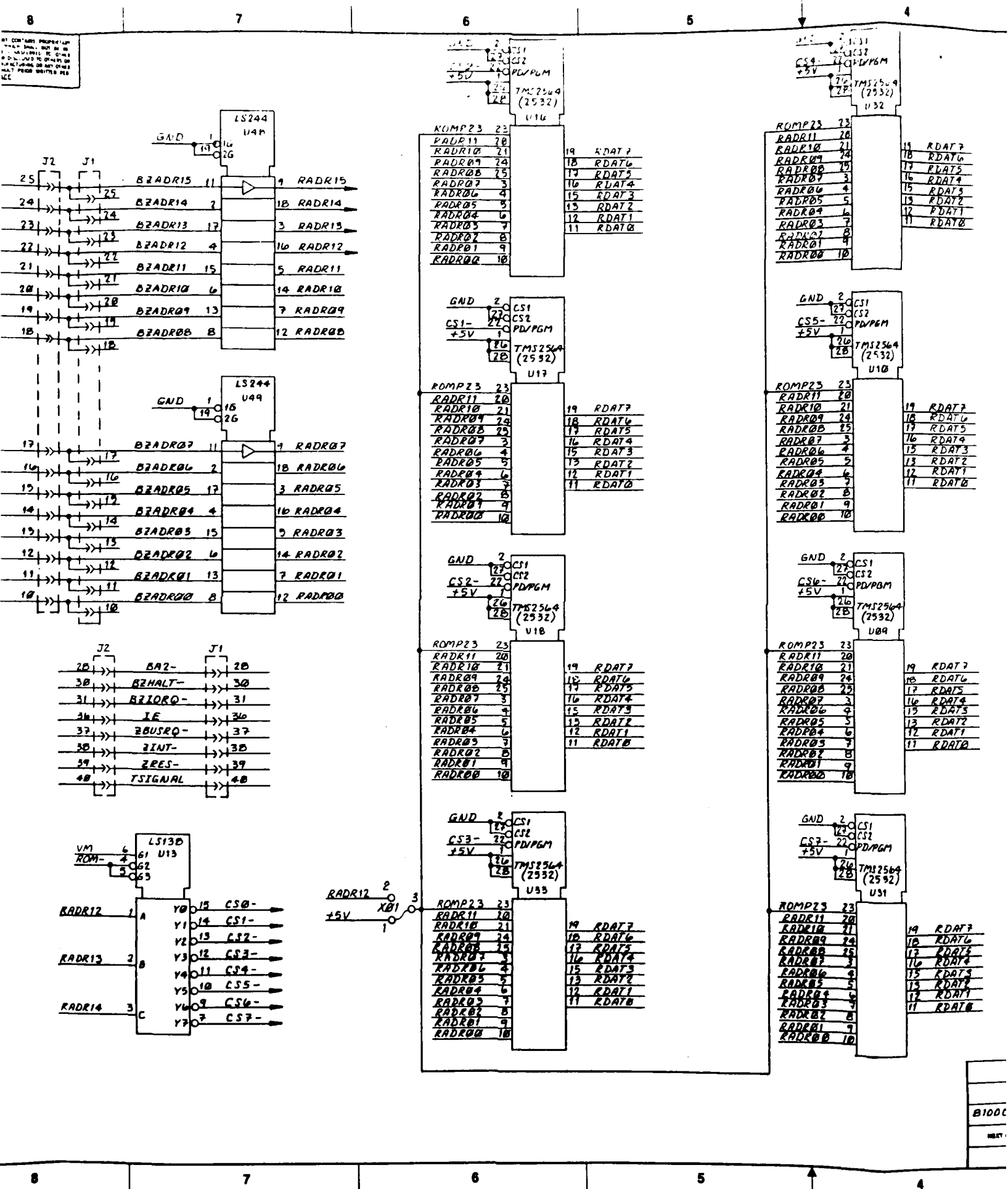
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B100096					

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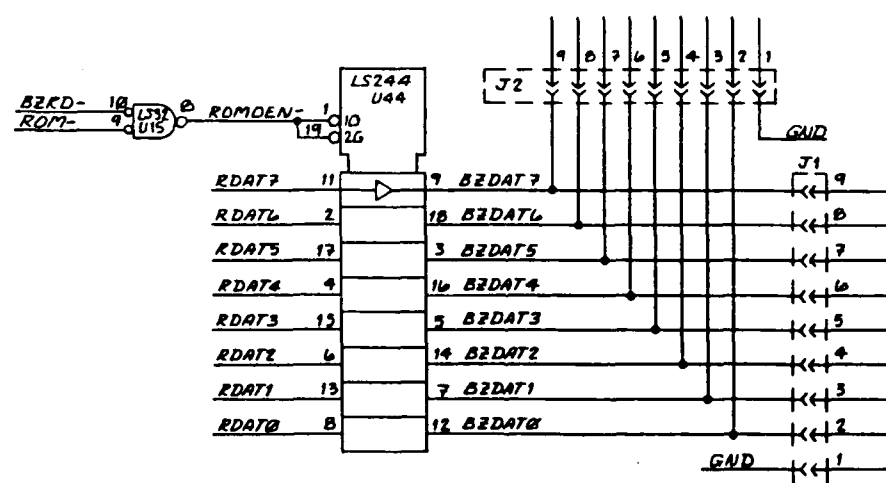
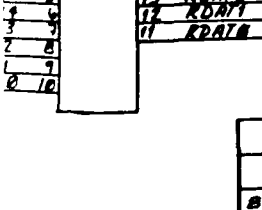
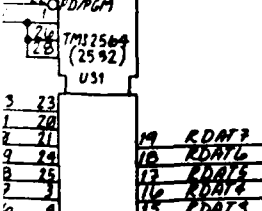
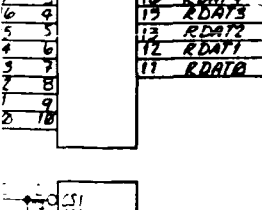
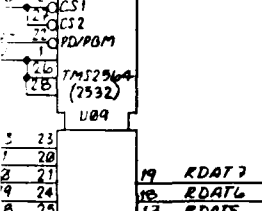
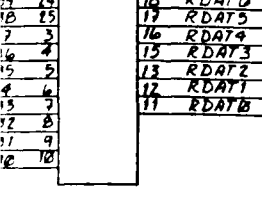
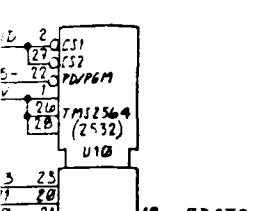
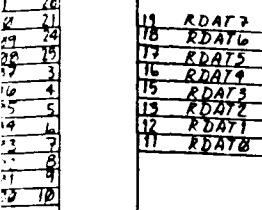
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	C4 C6				C5* D4*		
	C7* D4				D5*		
	D6			ROM~	B3 B8		
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	C7* D4				C4 C6		
	D6				D4 D6		
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4	3	2	1
1. CS1 2. CS2 3. PD/PGM 4. TMS1564 (2532) 5. U52		Dwg No. 2600430 Rev 1 Date 8/30/81	



QTY FROM PART OR IDENTIFICATION NO.		CONTRACT NO.		PARTS LIST	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES		APPROVALS DESIGNED: WEDDELL CHECKED: R. R. R. DATE: 5/15/81		ASSOCIATED COMPUTER CONSULTANTS Santa Barbara, California 93101	
MATERIAL:		DATE: 5/15/81		LOGIC DIAGRAM XQ/CP M BOARD	
PART: B100097		DATE: 5/15/81		Dwg No. 2600430	
NEXT ASSY:		USED ON:		SHEET 1 OF 2	
APPLICATION:		DO NOT SCALE DRAWING		SCALE: AS SHOWN	

2600430 1 C

SIGNAL NAME LOCATIONS - DRAWING 262243J
PAGE 22 of 22

* - SOURCED AT INDICATED COORDINATE OR PAGE

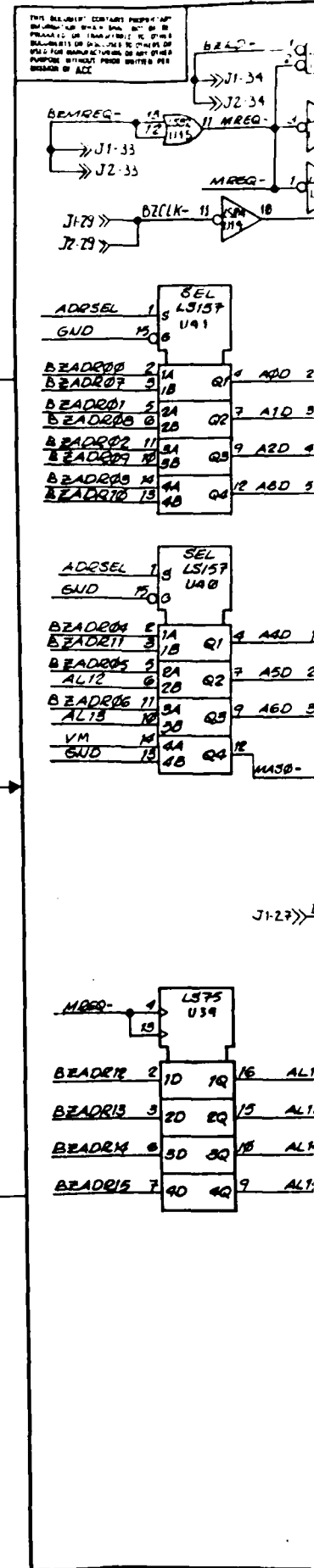
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A5	C7* D5			RAMOEN-	C4 D4		
A5D	C7*				D7*		
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	D8			RCAS0-	B6* C7		
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AL13	B7 C8			RCAS1-	B6* C7		
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AL15	B7			ROM-	B6*		
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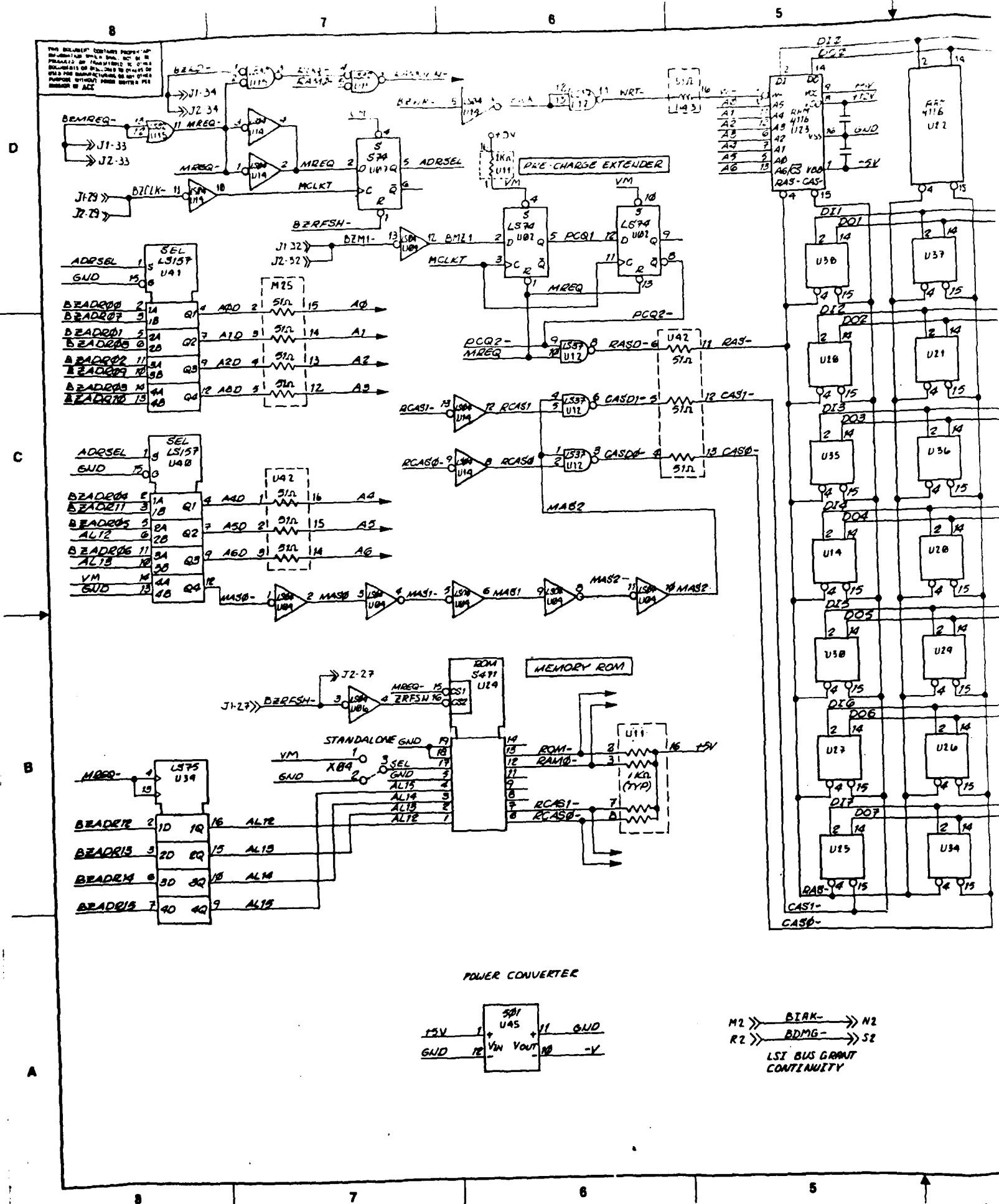
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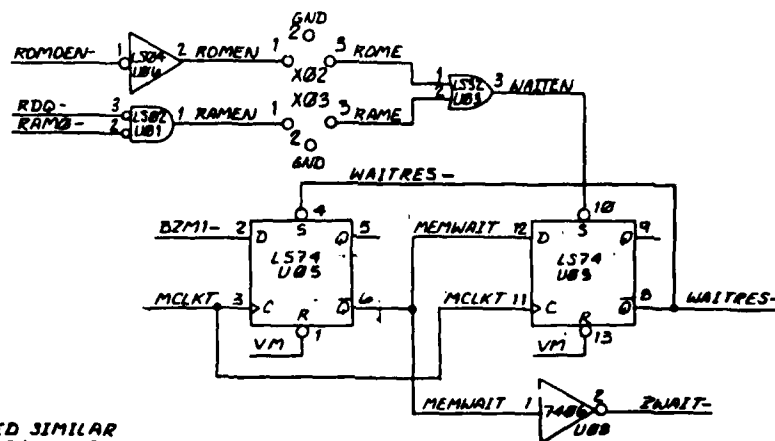
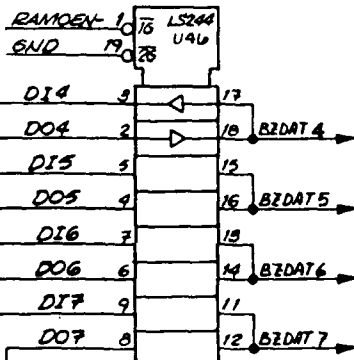
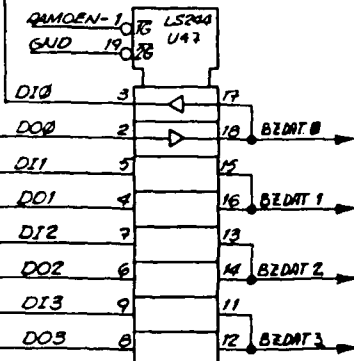
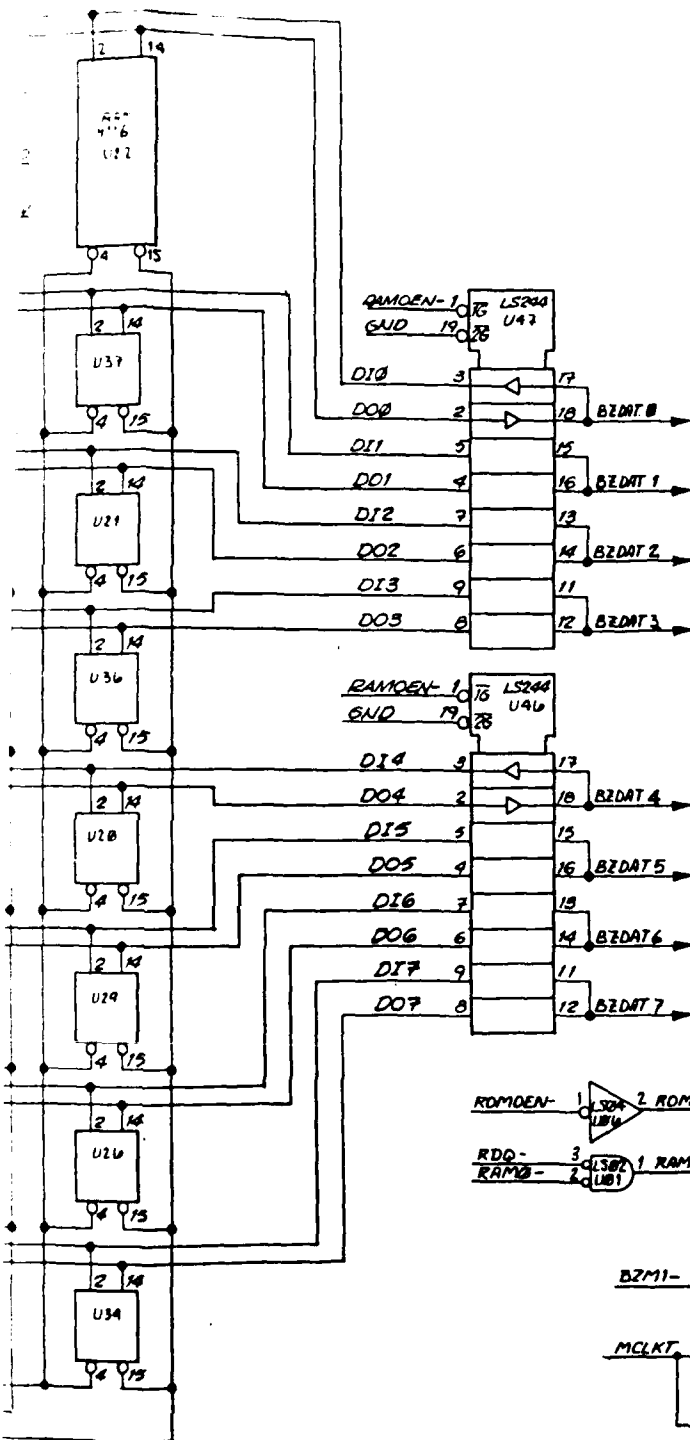
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A





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REVISIONS					
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B	10/10/61	CHNG. U14 PIN 5 24V TO 24V. 10/10/61	JM	AK	AK
C	10/10/61	ECO 2229-05	JM	AK	AK



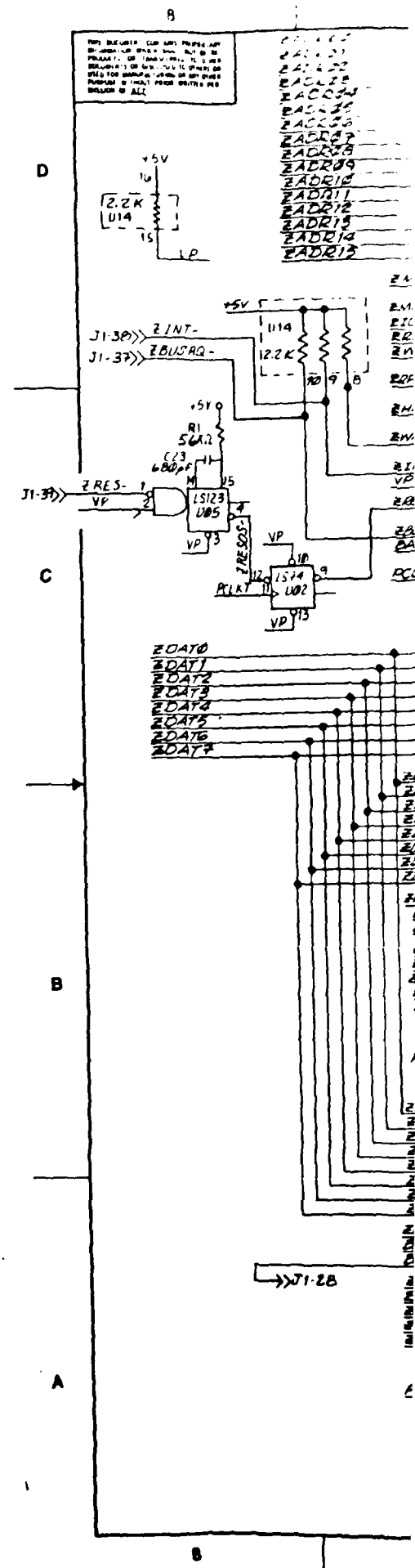
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ALL RAM SOCKETS WIRED SIMILAR
TO DETAIL OF P1. .1mf BYPASS
ON -5V AND +12V, ALL RAM CIRCUITS.

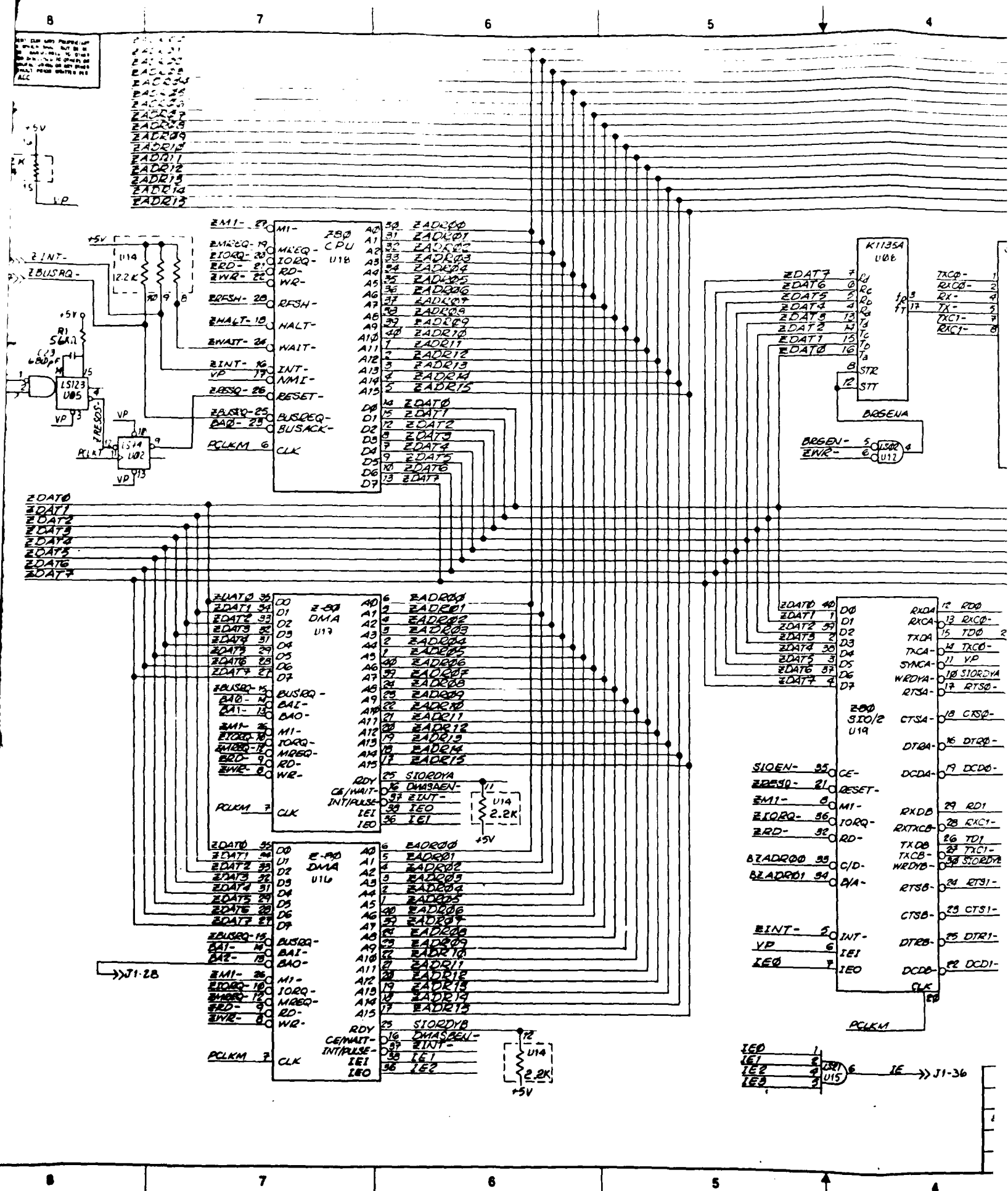
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FINISH		DRAWN		CHECKED	
TEST ANY		DESIGNED		DATE	
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B100097		J. MOORE		10/10/61	
NEXT ANY		RR		10/10/61	
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				2600430	
				C	
				PAGE 2 OF 2	

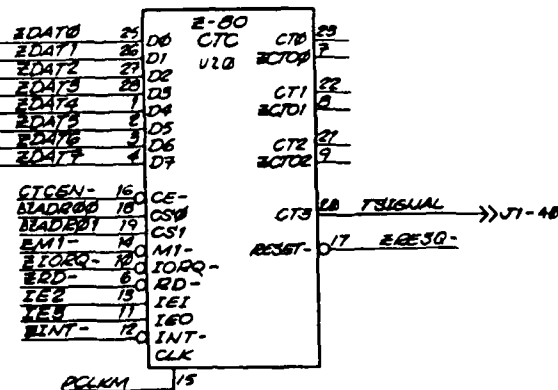
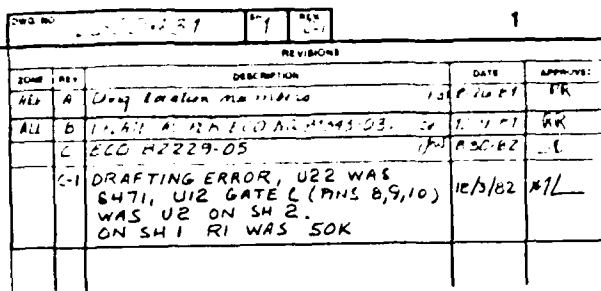
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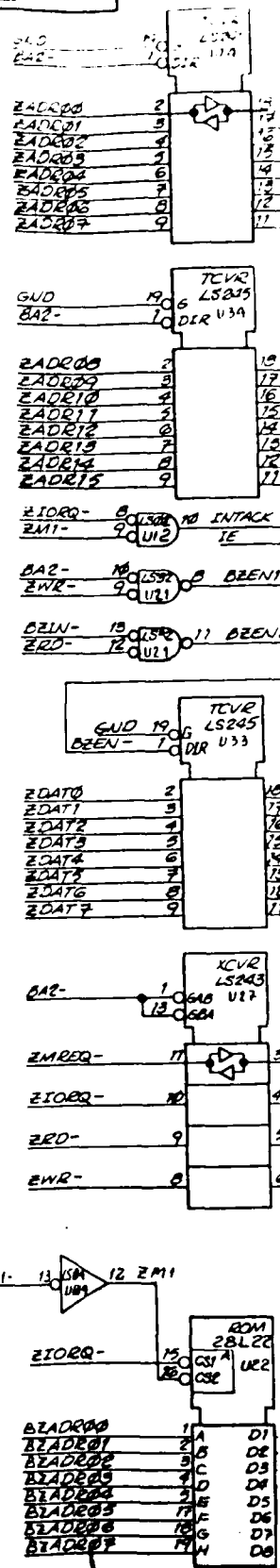
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ZBUSRQ- . . .	A7* B7* C7 C8		
ZDAT8 . . .	B2* B5* B7* C3 C5 C6* C8		
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ZRD- . . .	A7* B2 B5 B7* D7*		
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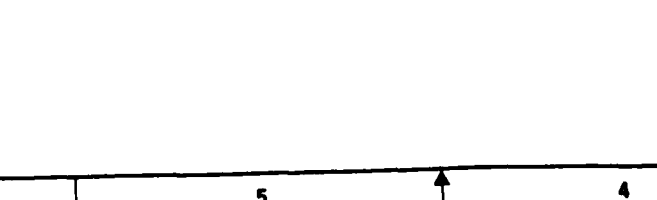
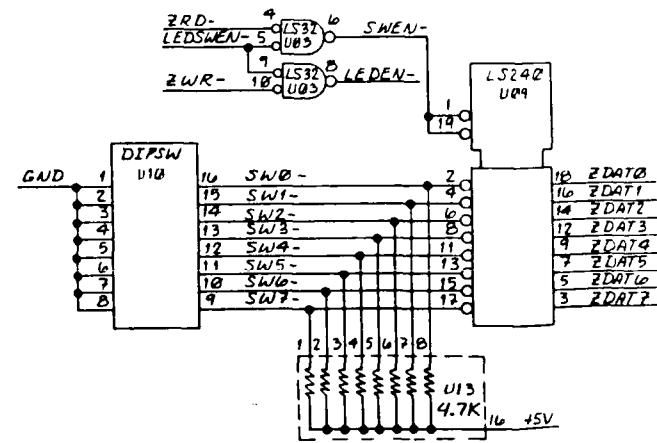
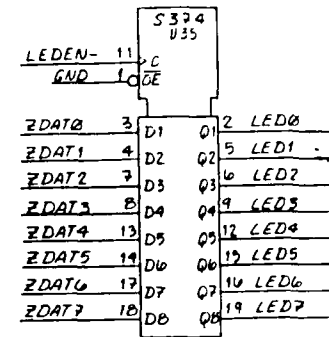
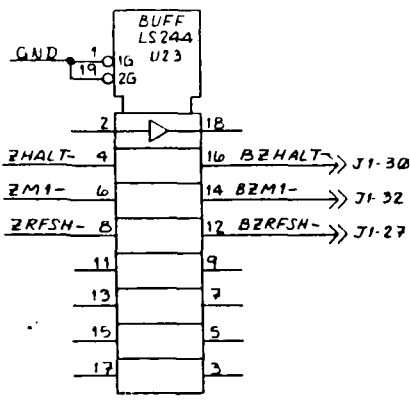
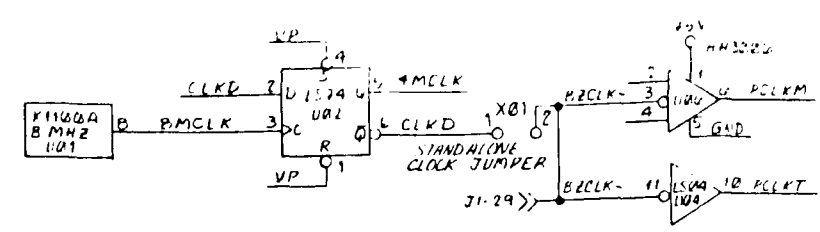
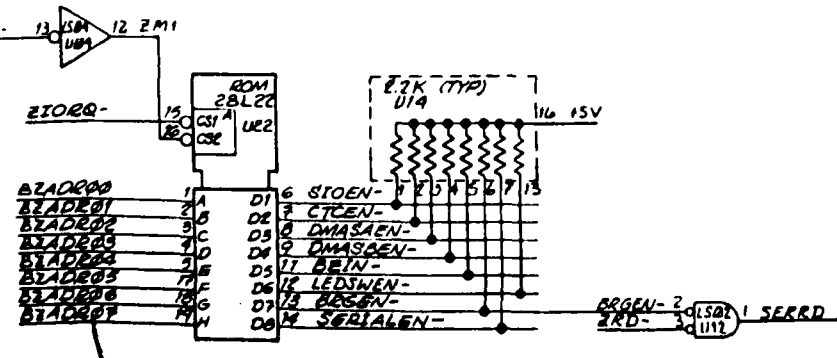
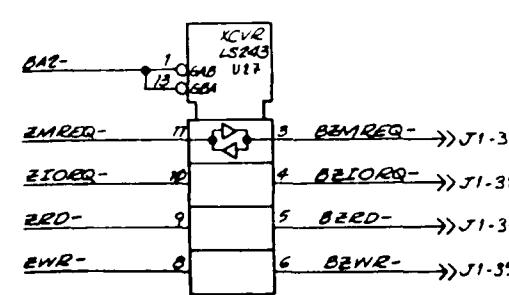
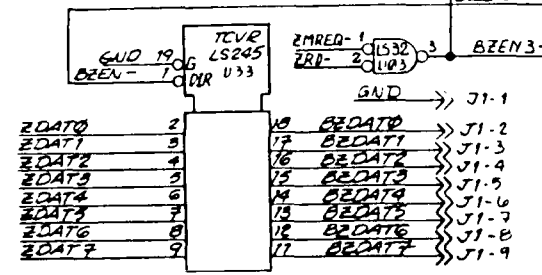
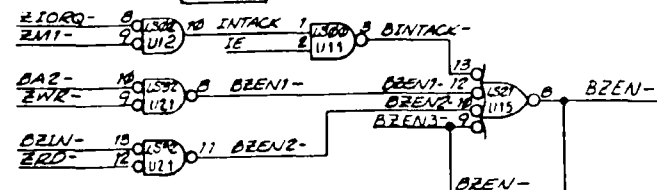
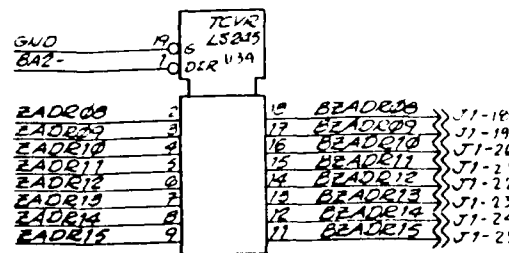
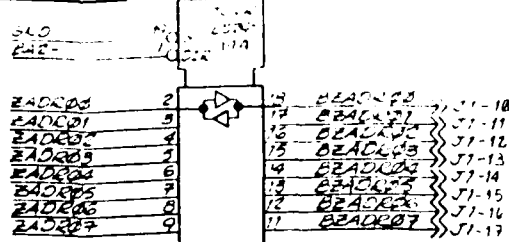


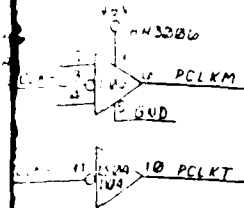


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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES 1 .01 1 .0005 1				CONTRACT NO.		<div style="text-align: center;">  <p>ASSOCIATED COMPUTER CONSULTANTS Sunnyvale, California 95091</p> </div>			
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NATERIAL		CHECKED R. R. Smith		19 May 81		<div style="text-align: center;"> <p><u>LOGIC DIAGRAM</u></p> <p>XQ/CP</p> <p>P BOARD</p> </div>			
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APPLICATION		DO NOT SCALE DRAWING		D		PCHG NO.		DUP. NO.	
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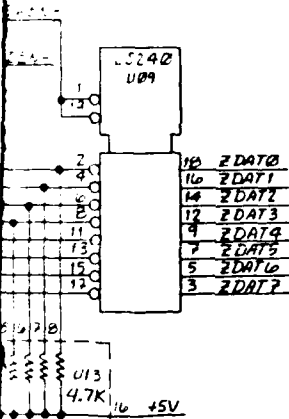
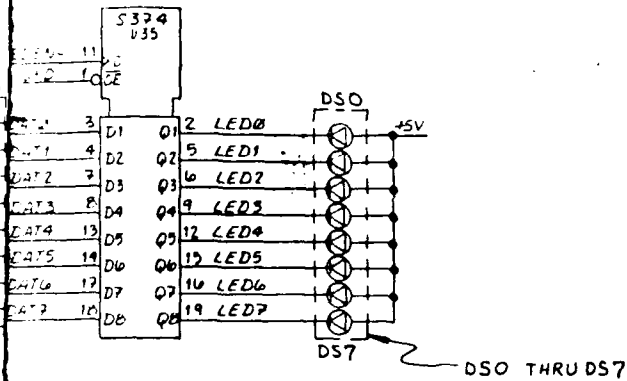
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION OF THE UNITED STATES GOVERNMENT. IT IS TO BE CONTAINED IN THE FILES OF THE RESEARCH AND DEVELOPMENT DIVISION OF THE ARMY RESEARCH OFFICE-DURHAM. IT IS TO BE CONTAINED IN THE FILES OF THE RESEARCH AND DEVELOPMENT DIVISION OF THE ARMY RESEARCH OFFICE-DURHAM. IT IS TO BE CONTAINED IN THE FILES OF THE RESEARCH AND DEVELOPMENT DIVISION OF THE ARMY RESEARCH OFFICE-DURHAM.





AM1 >> BIAK- >> A1.2
 AR2 >> BDMG- >> AS2

LSI BUS
 GRANT CONTINUITY



ZONE	REV	DESCRIPTION	DATE	APPROVED
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B	1	ECU R2229-05	10/1/84	JK
C	1	SEE SH1		

D

C

B

2600431

QTY REQD		PCEM NO.		PART OR IDENTIFIER NO.		NOMENCLATURE OR DESCRIPTION		MATERIAL SPECIFICATION	
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1796 1797 1798 1799 1800 1801 1802 1803 1804 1805 1806 1807 1808 1809 1810 1811 1812 1813 1814 1815 1816 1817 1818 1819 1820 1821 1822 1823 1824 1825 1826 1827 1828 1829 1830 1831 1832 1833 1834 1835 1836 1837 1838 1839 1840 1841 1842 1843 1844 1845 1846 1847 1848 1849 1850 1851 1852 1853 1854 1855 1856 1857 1858 1859 1860 1861 1862 1863 1864 1865 1866 1867 1868 1869 1870 1871 1872 1873 1874 1875 1876 1877 1878 1879 1880 1881 1882 1883 1884 1885 1886 1887 1888 1889 1890 1891 1892 1893 1894 1895 1896 1897 1898 1899 1900 1901 1902 1903 1904 1905 1906 1907 1908 1909 1910 1911 1912 1913 1914 1915 1916 1917 1918 1919 1920 1921 1922 1923 1924 1925 1926 1927 1928 1929 1930 1931 1932 1933 1934 1935 1936 1937 1938 1939 1940 1941 1942 1943 1944 1945 1946 1947 1948 1949 1950 1951 1952 1953 1954 1955 1956 1957 1958 1959 1960 1961 1962 1963 1964 1965 1966 1967 1968 1969 1970 1971 1972 1973 1974 1975 1976 1977 1978 1979 1980 1981 1982 1983 1984 1985 1986 1987 1988 1989 1990 1991 1992 1993 1994 1995 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NAME	COORD	ALT PAGES	NAME	COORD	ALT PAGES
+SV	B8 C8		RXC0H	C7	
CTS0-	D6*		RXC0HI	C8*	
CTS0H	D7	01	RXC0L	C7	
CTS0HI	D8*		RXC0LI	C8*	
CTS0L	D7		RXC1H	B7	
CTS0LI	D8*		RXC1HI	B8*	
CTS1-	C6*		RXC1L	B7	
CTS1H	C7	01	RXC1LI	B8*	
CTS1HI	C8*		RXD0H	D7	
CTS1L	C7		RXD0HI	D8*	
CTS1LI	C8*		RXD0L	D7	
DCD0-	C6*		RXD0LI	D8*	
DCD0H	C7	01	RXD1H	C7	
DCD0HI	C8*		RXD1HI	C8*	
DCD0L	C7		RXD1L	C7	
DCD0LI	C8*		RXD1LI	C8*	
DCD1-	B6*		SERRD	C7 D7	
DCD1H	B7	01	TCM0-	D6*	
DCD1HI	B8*		TCM0H	D7*	
DCD1L	B7		TCM0HI	D8*	
DCD1LI	B8*		TCM0L	C7*	
DSR0H	D7		TCMBLI	C8*	
DSR0HI	D8*		TCM1-	B6*	
DSR0L	D7		TCM1H	B7*	
DSR0LI	D8*		TCM1HI	B8*	
DSR1H	C7		TCM1L	B7*	
DSR1HI	C8*		TCM1LI	B8*	
DSR1L	C7		TCT0H	C4*	
DSR1LI	C8*		TCT0HO	C3*	
DTR0-	C5		TCT0L	C4*	01*
DTR0H	C4*	01*	TCT0LO	C3*	
DTR0HI	C3*		TCT1H	B4*	
DTR0L	C4*	01*	TCT1HO	B3*	
DTR0LO	C3*		TCT1L	B4*	
DTR1-	B5		TCT1LO	B3*	
DTR1H	B4*	01*	TDB	D5	
DTR1HI	B3*		TD1	C5	01*
DTR1L	B4*	01*	TT0-	C5	01*
DTR1LO	B3*		TT1-	B5	01*
GND	B3 B4		TXD0H	D4*	01*
	B5 B8		TXD0HO	D3*	
	C3 C4		TXD0L	D4*	01*
	C5 C8		TXD0LO	D3*	
	D4 D5		TXD1H	C4*	
	D8		TXD1HO	C3*	
			TXD1L	B4*	01*
			TXD1LO	B3*	
			VP	B4 B7	
				C4 C7	
				D4 D7	
MDBC	B3* C5		ZDAT0	D6*	
MDBD	B3* B5		ZDAT1	C6*	01* 02*
MDCA	D3* D5		ZDAT2	D6*	01* 02*
MDCB	C3* C5		ZDAT3	C6*	
RC0-	C6*				
RC1-	B6*	01			
RD0	D6*	01			
RD1	C6*	01			
RNG0H	D7*	01			
RNG0HI	D8*				
RNG0L	D7*				
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RTS0-	D5				
RTS0H	D4*	01*			
RTS0HO	D3*				
RTS0L	D4*	01*			
RTS0LO	D3*				
RTS1-	B5				
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RTS1LO	B3*				

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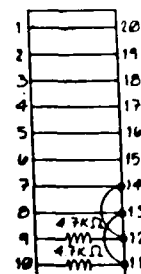
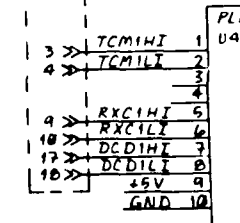
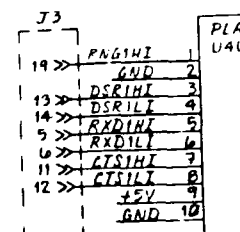
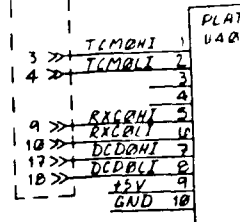
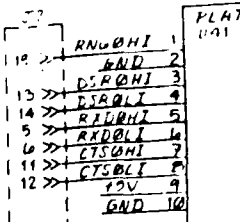
C

B

A

THIS DRAWING IS FOR THE PURPOSE OF
IDENTIFYING THE LOCATION OF THE
DRAWING IN THE DRAWING SET
FOR THE DRAWING SET. IT IS NOT
TO BE USED FOR ANY OTHER PURPOSE.
DRAWING SET MUST BE KEPT
SEPARATE OF ALL

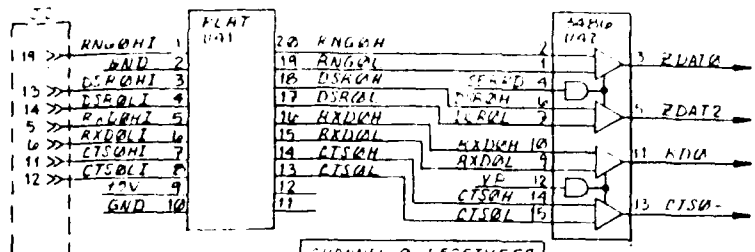
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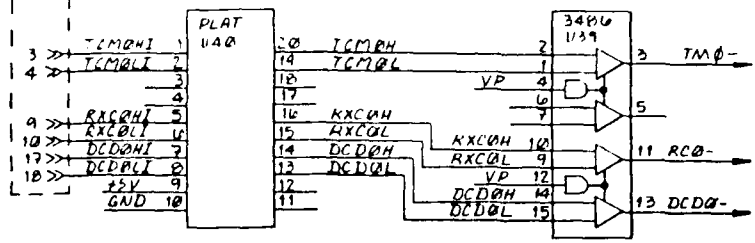
RS-22
RECEIVE
PLATFORM
ACC PART NO
5000139

8

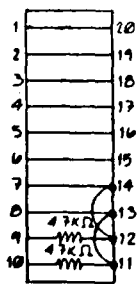
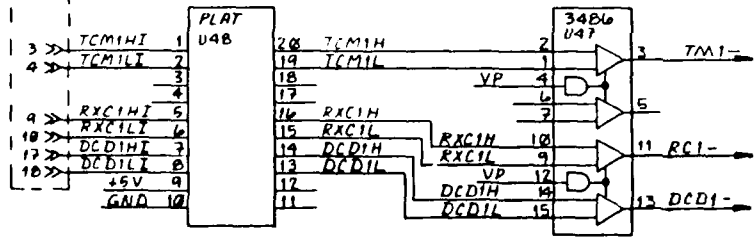
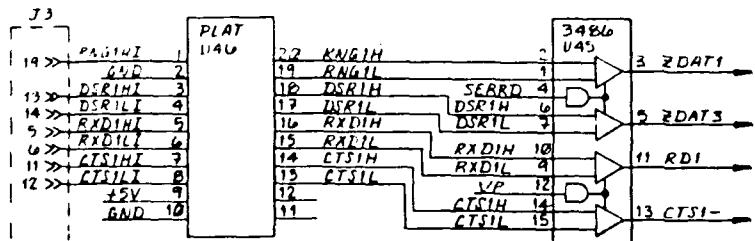
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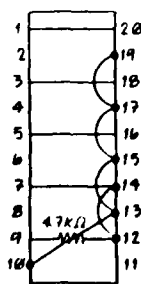
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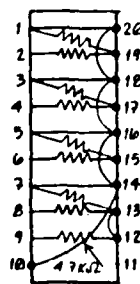
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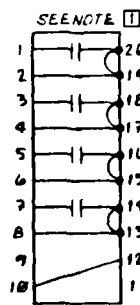
RS-422
RECEIVE
PLATFORM
ACC PART NO.
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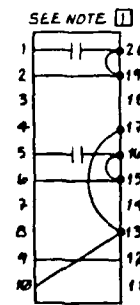
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ACC PART NO.
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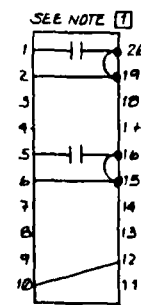
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RECEIVE
PLATFORM
ACC PART NO.
5000141



RS-422
DRIVER
PLATFORM
ACC PART NO.
5000142



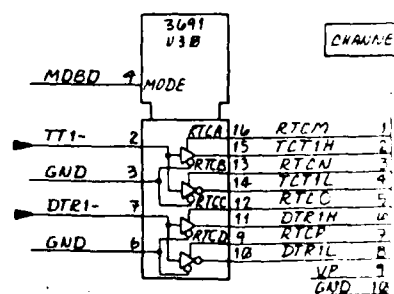
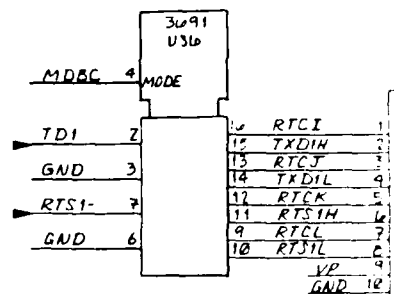
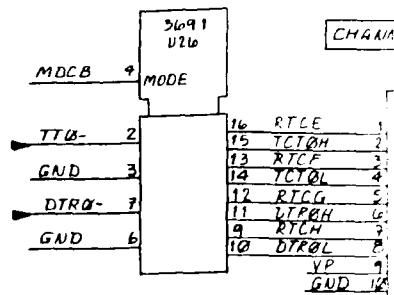
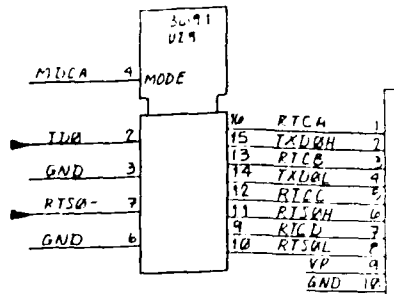
RS-423
DRIVER
PLATFORM
ACC PART NO.
5000143



MIL-STD 188/114
DRIVER
PLATFORM
ACC PART NO.
5000144



TYP FOR
ABOVE



NOTES
1 CAPACITORS SHOWN FOR REFERENCE
MAY BE FIELD INSTALLED BY CUS

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MISSION OF ACC

CHANNEL A

PM2	1	ATXD	2	JA
>	6	ARXD	3	>
>	17	ARTS	4	>
>	12	ACTS	5	>
>	14	ADSR	6	>
>	122	SIG GND	7	>
>	18	ADCD	8	>
>	4	ATCM	15	>
>	10	ARXC	17	>
>	15	ADTR	20	>
>	19	ARNG	22	>
>	20	ATCT	24	>

QTY	RECD	FS
UNLESS OTHERWISE DIMENSIONS ARE IN TOLERANCES ARE:		
FRACTIONS	DEC	
±	.XX	.XXX
MATERIAL		
FINISH		
DO NOT S		

	6800064
NEXT ASSY	USED ON
APPLICATION	

DWG NO 2500027

REV


REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	FIG. ECO # 82229-05	13-SEP-81	(signature)

CHANNEL B

PM1	1	BTXD	2	JB
>	6	BRXD	3	>
>	7	BRTS	4	>
>	12	BCTS	5	>
>	14	BDSR	6	>
>	22	SIG GND	7	>
>	18	BDOD	8	>
>	4	BTCLM	15	>
>	10	BRXC	17	>
>	15	BDTR	20	>
>	19	BRNG	22	>
>	20	BTCT	24	>

2500027
A

QTY REQD	FSCM NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION		MATERIAL SPECIFICATION	
PARTS LIST						
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS ± DECIMALS .XX ± ANGLES ± .XXX ±			CONTRACT NO.		 ASSOCIATED COMPUTER CONSULTANTS Santa Barbara, California 93101	
MATERIAL			APPROVALS	DATE	SCHEMATIC XQ/CP RS-232 DUAL CHANNEL ONLY	
FINISH			DRAWN	10-26-81	SIZE B FSCM NO. DWG. NO. 2500027 REV. A	
DO NOT SCALE DRAWING			CHECKED	10 OCT 81	SCALE SHEET	
			ISSUED	10 30-81		

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USED FOR MANUFACTURING OF ANY OTHER
PURPOSE WITHOUT PRIOR WRITTEN PER-
MISSION OF ACC

CHANNEL A

PM1	2	ASD-A	4	JA
>	4	AST-A	5	>
>	6	ARD-A	6	>
>	8	ARS-A	7	>
>	10	ART-A	8	>
>	12	ACS-A	9	>
>	14	ADM-A	11	>
>	16	ATR-A	12	>
>	18	ARR-A	13	>
>	19	AIC-A	15	>
>	21	ATT-A	17	>
>	22	ASG	19	>
>	1	ASD-B	22	>
>	3	AST-C	23	>
>	5	ARD-C	24	>
>	7	ARS-B	25	>
>	9	ART-C	26	>
>	11	ACS-C	27	>
>	13	ADM-C	29	>
>	15	ATR-B	30	>
>	17	ARR-C	31	>
>	20	ATT-B	35	>

CHANNEL E

QTY REQD	
UNLESS OTHER DIMENSIONS AP- TOLERANCES A FRACTIONS 0 ± . J	
MATERIAL	
FINISH	
NEXT ASSY	USED ON
APPLICATION	
DO NOT	

DWG NO.

REVISED

SH

REV

REVISIONS

REV

DESCRIPTION

DATE

APPROVED

4

REF ECO # 82229-05

13-SEP-82

1

ANEL B

PM2	2	BSD-A	4	JB
	4	BST-A	5	
	6	BRD-A	6	
	8	BRS-A	7	
	10	BRT-A	8	
	12	BCS-A	9	
	14	BDM-A	11	
	16	BTR-A	12	
	18	BRR-A	13	
	19	BIC-A	15	
	21	BTT-A	17	
	22	BSG	19	
	1	BSD-B	22	
	3	BST-C	23	
	5	BRD-C	24	
	7	BRS-B	25	
	9	BRT-C	26	
	11	BCS-C	27	
	13	BDM-C	29	
	15	BTR-B	30	
	17	BRR-C	31	
	20	BTT-B	35	

2500028

A

QTY REQD	FSCM NO.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION		MATERIAL SPECIFICATION	
PARTS LIST						
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:			CONTRACT NO.			
FRACTIONS DECIMALS ANGLES ± .XX ± .XXX ±			APPROVALS DATE			
MATERIAL			DRAWN RODENWALD 10-27-81			
FINISH			CHECKED R Russ 30 Oct 81			
DO NOT SCALE DRAWING			ISSUED (W) 10-30-81	SIZE B	FSCM NO.	DWG. NO. 2500028
			SCALE	SHEET		REV. A

ACC

ASSOCIATED COMPUTER CONSULTANTS
Santa Barbara, California 93101

SCHEMATIC
XQ/CP

RS-422 DUAL CHANNEL ONLY